

# Communicating State Transition Systems for Fine-Grained Concurrent Resources

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# Reasoning about shared-memory concurrency

# How to model shared-memory concurrency

Two views at  
shared-memory concurrency

# Coarse-Grained Concurrency

*Locks* (or CCRs) are given  
as a primitive for synchronization.

# Fine-Grained Concurrency

Synchronization is implemented via atomic *Read-Modify-Write* commands.

**Two powerful tools  
for reasoning**

# Concurrent Separation Logic

O'Hearn [CONCUR'07], Brookes [CONCUR'04]

# Rely Guarantee Reasoning

Jones [TOPLAS'83]



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- The protocol for interference is fixed:  
*Conditional Critical Regions with Resource Invariants*

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- The protocol for interference is fixed:  
*Conditional Critical Regions with Resource Invariants*
- *Interference* doesn't matter: CCR handle it

$$\frac{\Gamma, r : I \vdash \{p\} c \{q\}}{\Gamma \vdash \{p * I\} \text{resource } r \text{ in } c \{q * I\}} \text{RESOURCECSL}$$

$$\frac{\Gamma, r : I \vdash \{p\} c \{q\}}{\Gamma \vdash \{p * I\} \text{ resource } r \text{ in } c \{q * I\}} \text{RESOURCECSL}$$

“resource creation”

$$\frac{\Gamma, r : I \vdash \{p\} c \{q\}}{\Gamma \vdash \{p * I\} \text{ resource } r \text{ in } c \{q * I\}} \text{RESOURCECSL}$$

$$\frac{\Gamma \vdash \{p_1\} c_1 \{q_1\} \quad \Gamma \vdash \{p_2\} c_2 \{q_2\}}{\Gamma \vdash \{p_1 * p_2\} c_1 \parallel c_2 \{q_1 * q_2\}} \text{PARCSL}$$

All interference  
is handled here

$$\frac{\boxed{\Gamma} \vdash \{p_1\} c_1 \{q_1\} \quad \boxed{\Gamma} \vdash \{p_2\} c_2 \{q_2\}}{\boxed{\Gamma} \vdash \{p_1 * p_2\} c_1 \parallel c_2 \{q_1 * q_2\}} \text{PARCSL}$$



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- One can define arbitrary protocols for process interference via *Guarantee relation*.

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- One can define arbitrary protocols for process interference via *Guarantee relation*.
- Interference matters!  
Atomic operations should be given specifications stable wrt *Rely relation*.

# “Forking/shuffling” parallel composition

$$\frac{R \vee G_2, G_1 \vdash \{p\} c_1 \{q_1\} \quad R \vee G_1, G_2 \vdash \{p\} c_2 \{q_2\}}{R, G_1 \vee G_2 \vdash \{p\} c_1 \parallel c_2 \{q_1 \wedge q_2\}} \text{PARRG}$$

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**Taking the best of two worlds**

**Our Approach**

**Fine-Grained Resources**



# Resources

## Fine-Grained

**Resources**

**State Invariants**

**Fine-Grained**

**Resources**

**State Invariants**

**Fine-Grained**

**Transitions**

**Resources**

**State Invariants**

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**Composition**

**Forking/shuffling**

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**Composition**

**Communication**

**Forking/shuffling**

**Subjectivity**

# State Transition Systems

Communication

Subjectivity

# State Transition Systems

## Communication

### Subjectivity

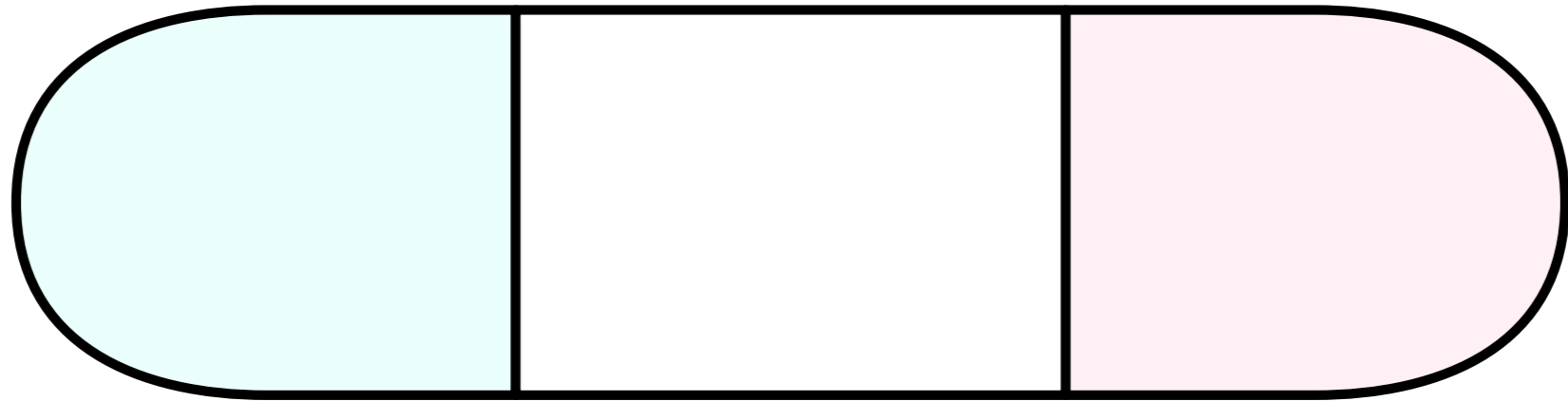
(Ley-Wild and Nanevski, POPL 2013)



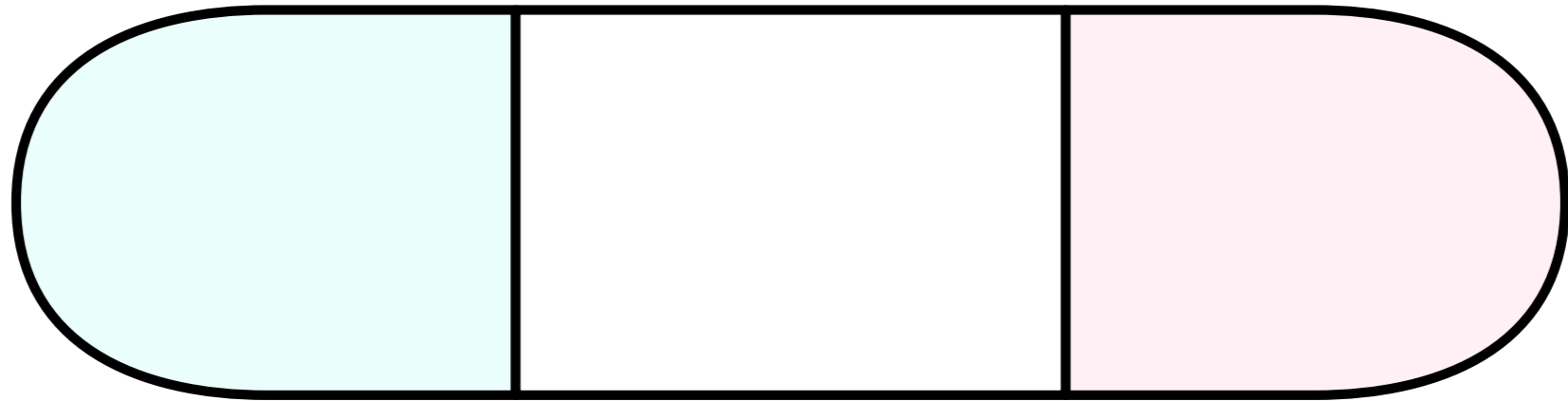
# Subjective Communicating State-Transition Systems

# Concurroids

# Concurroid States

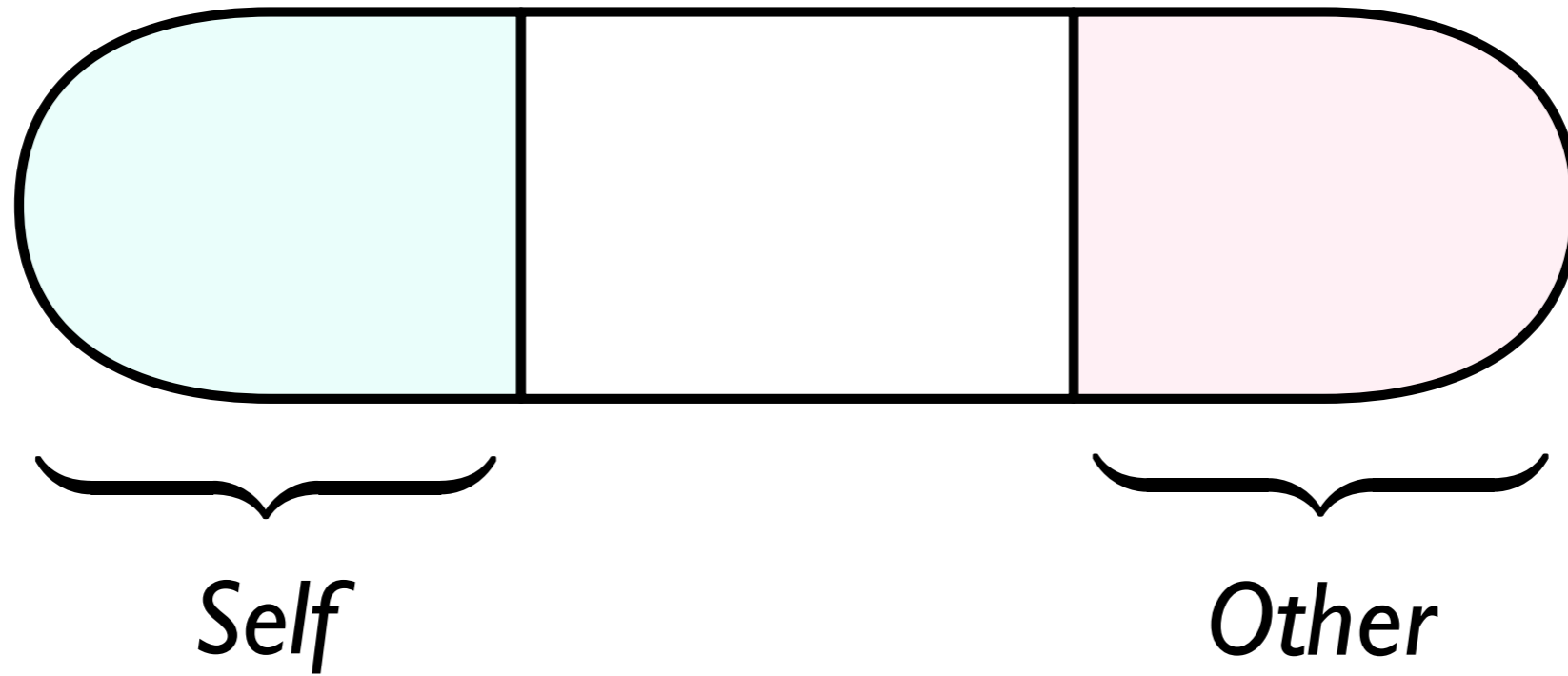


# Concurroid States

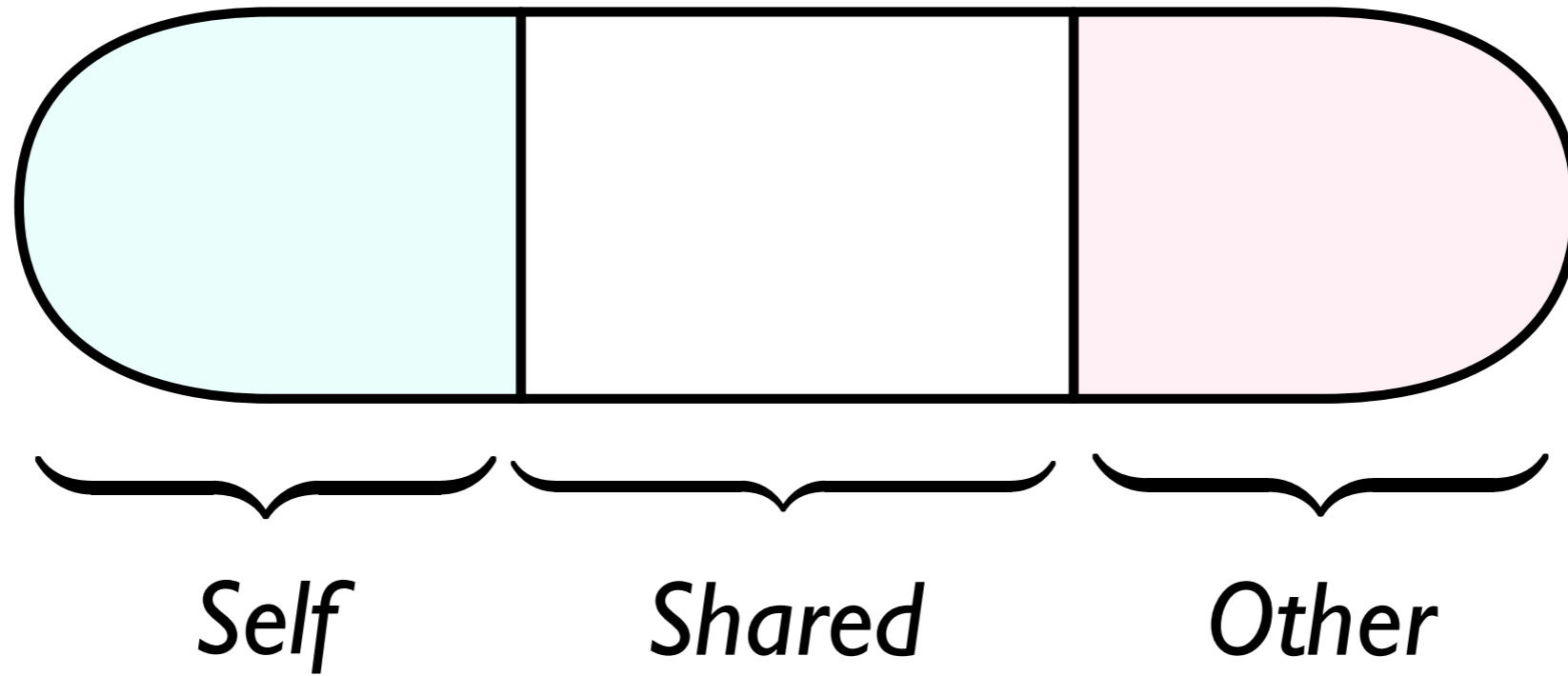


*Self*

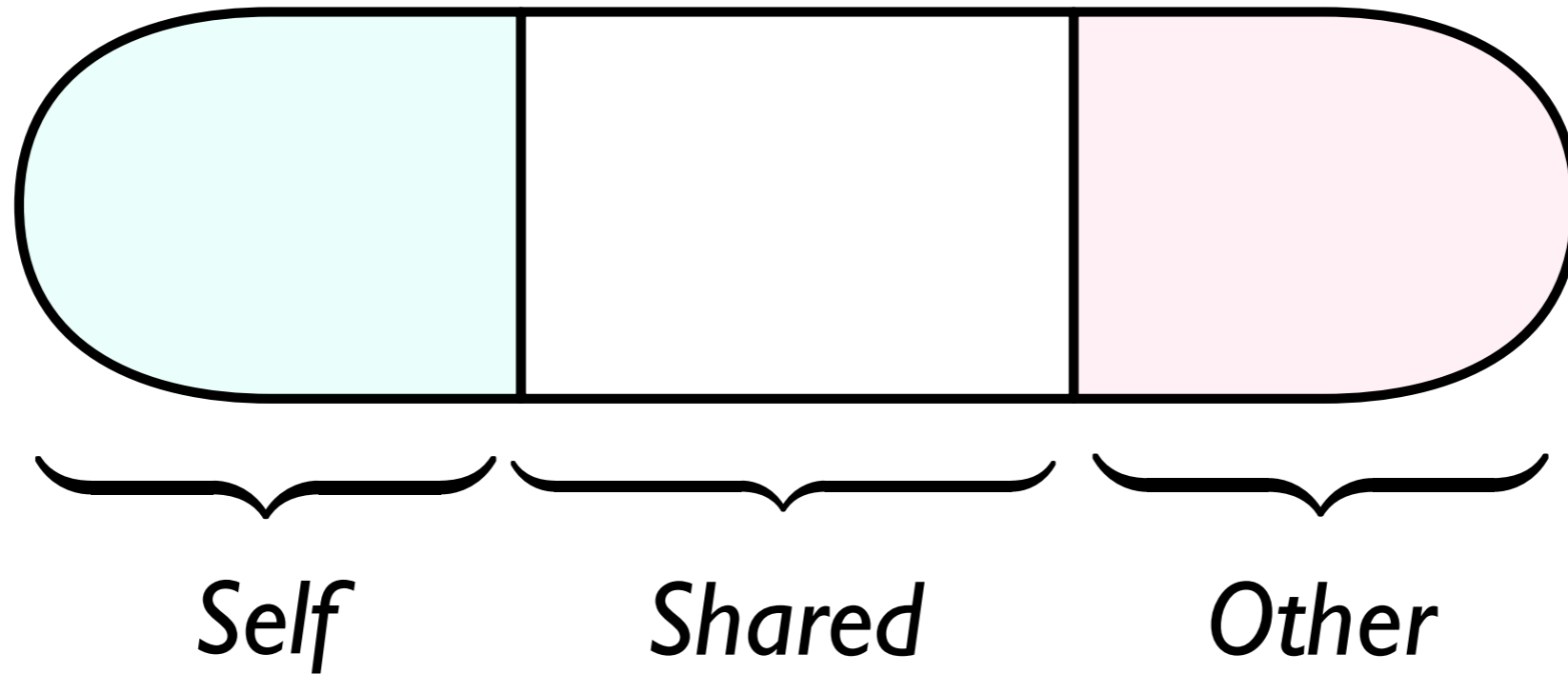
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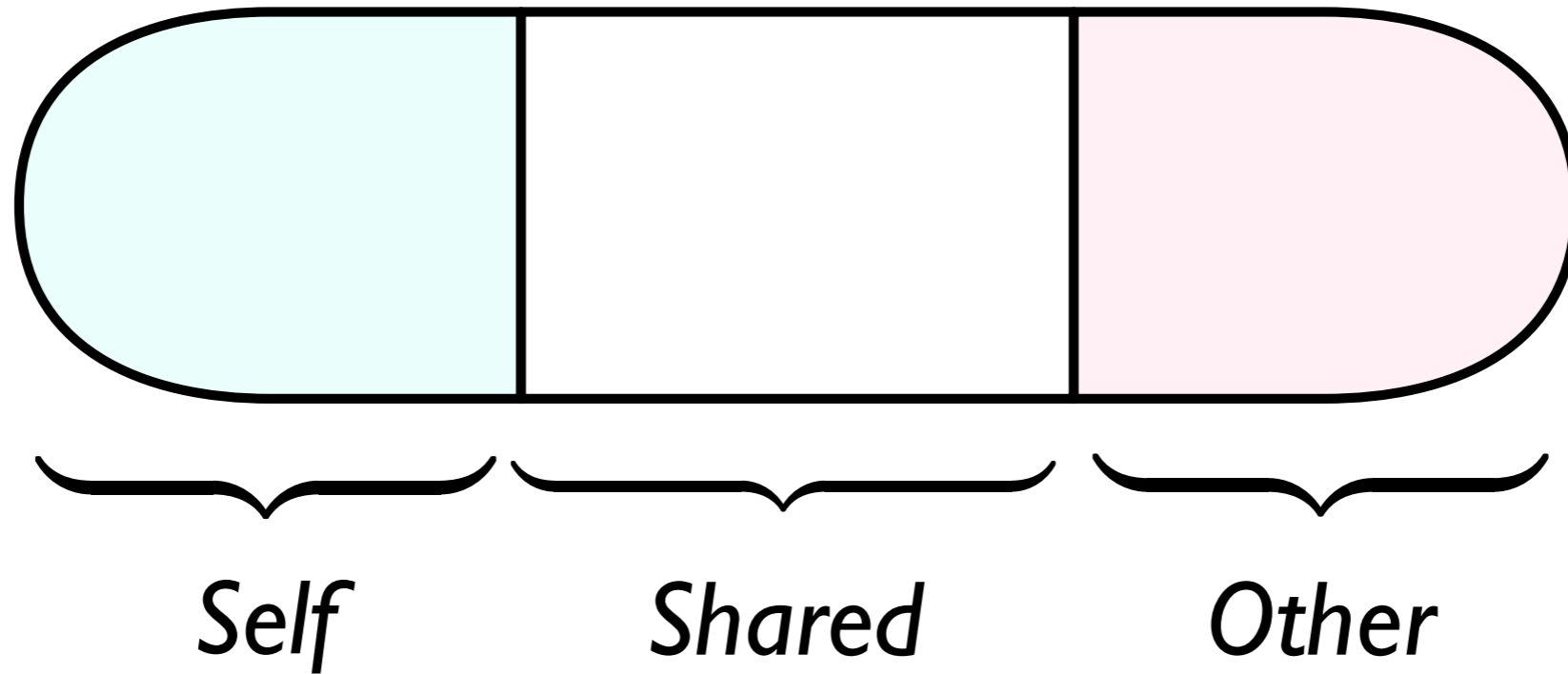


# Concurroid States



- *Self* - owned by me

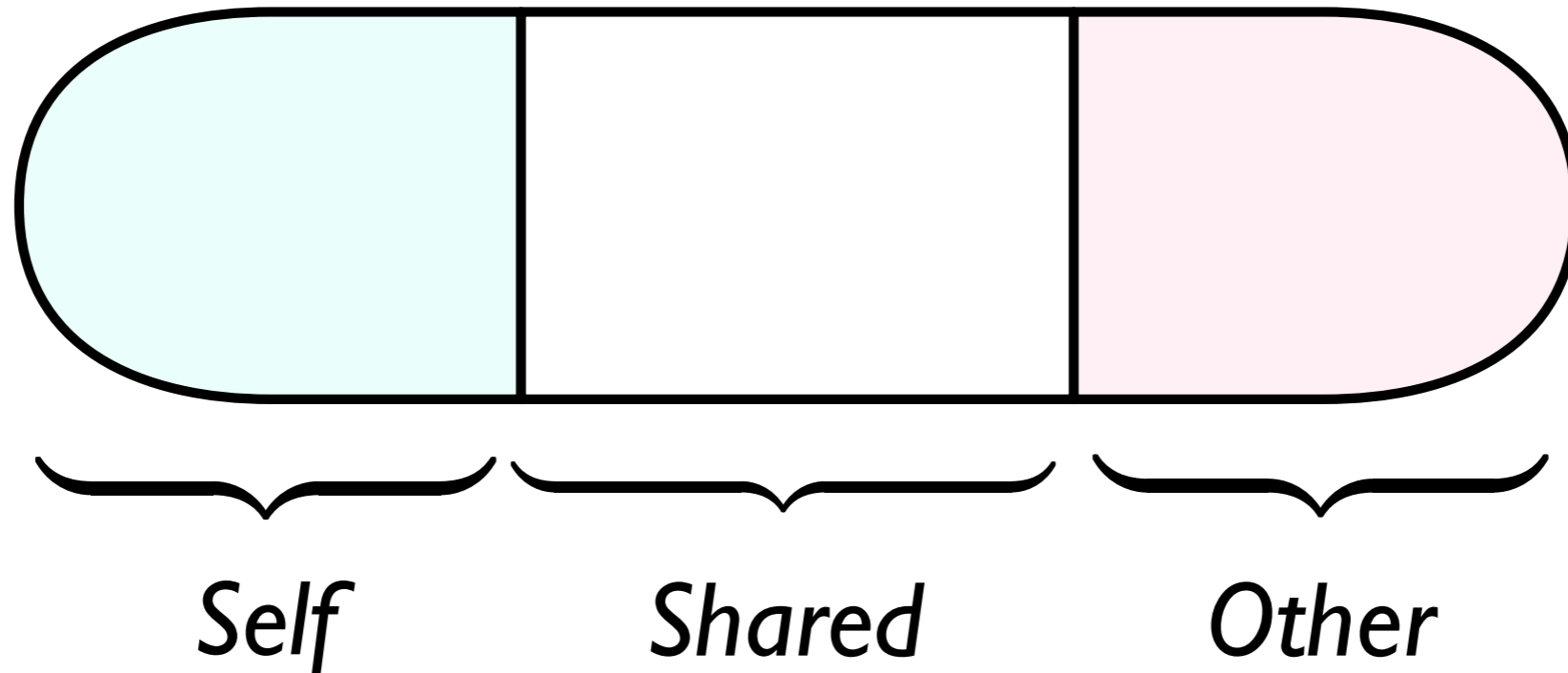
# Concurroid States



- *Self* - owned by me
- *Other* - owned by all others

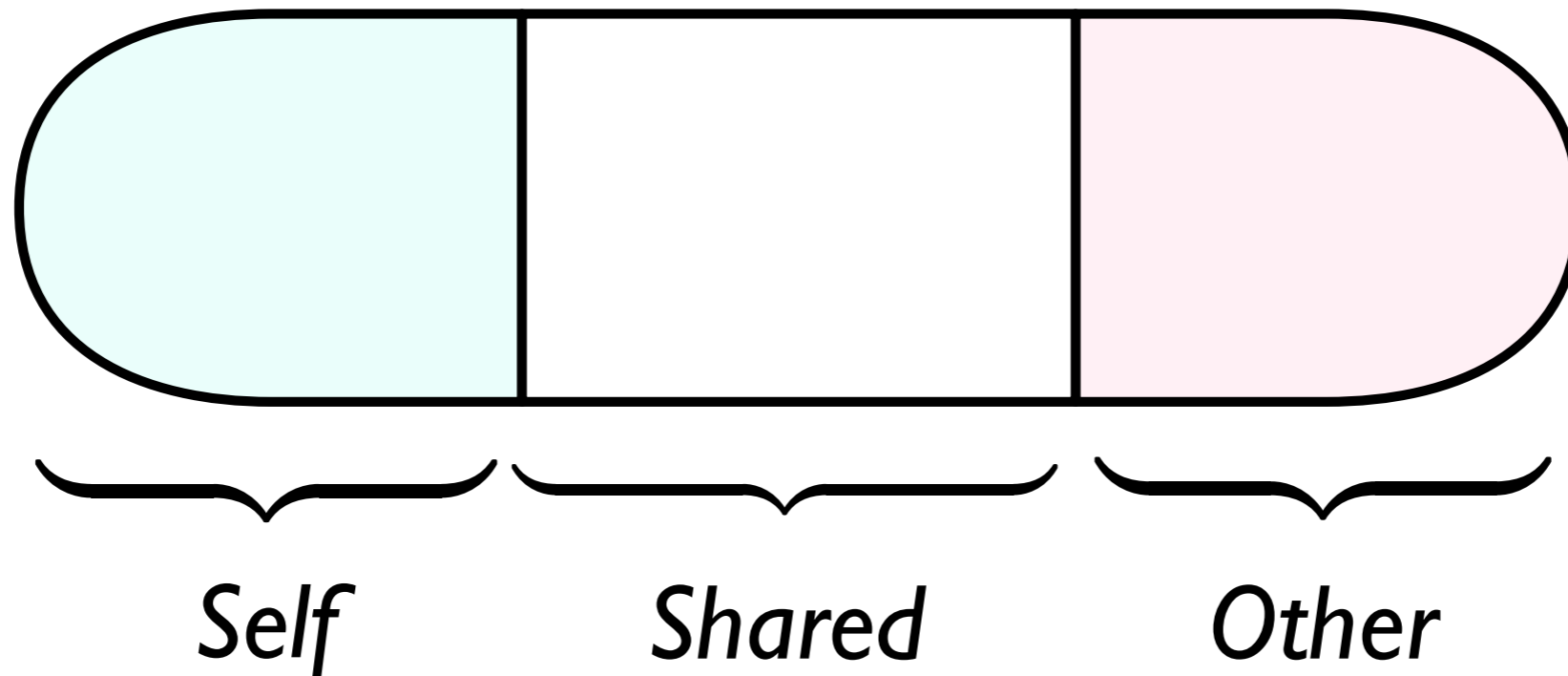


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- *Self* - owned by me
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- *Shared* - owned by the resource

# Concurroid States



- *Self* - owned by me
- *Other* - owned by all others
- *Shared* - owned by the resource
- Self and Other are elements of a *Partial Commutative Monoid* (PCM):  $(S, \mathbf{0}, \oplus)$ .

# Building a concurrent for Ticketed Lock








$n_1$



$$n_1 \leq n < n_2$$






owner 


$$n_1 \leq n < n_2$$





owner 

$$n_1 \leq n < n_2$$

next 





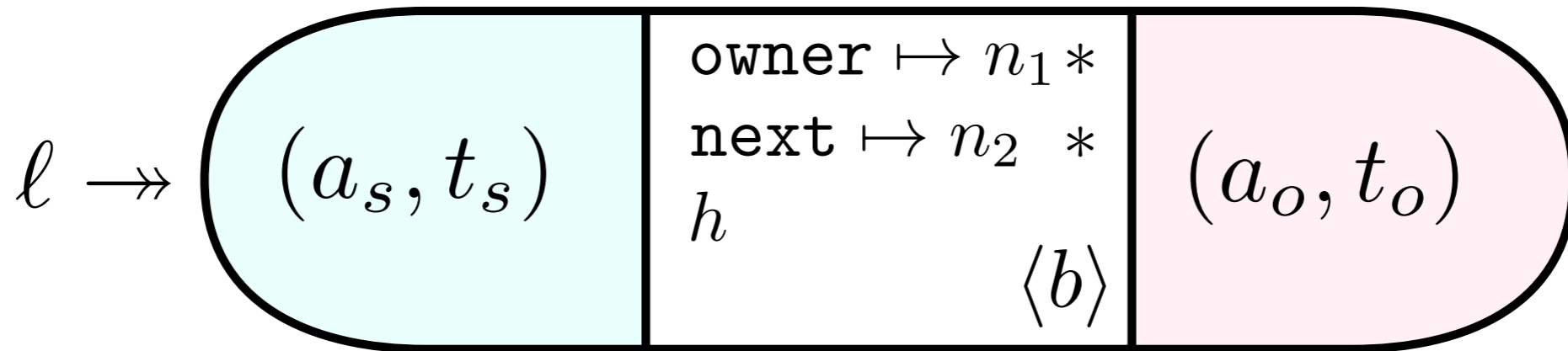
# Reference Implementation

```
lock = {  
    x := DRAW;  
    while (!TRY(x)) SKIP;  
}
```

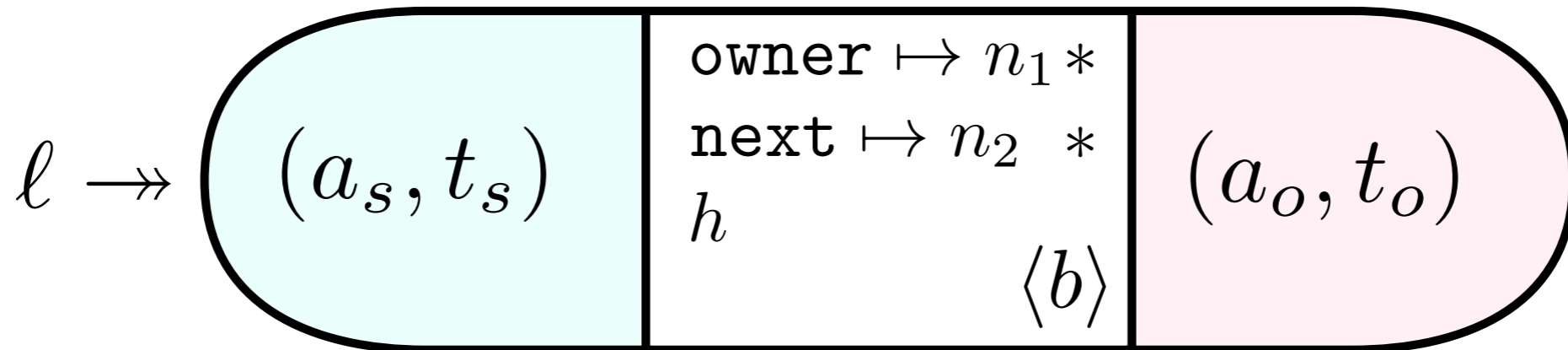
```
unlock = {  
    INCR_OWN;  
}
```

```
DRAW      = { return FETCH_AND_INCREMENT(next); }  
TRY(n)    = { return (n == owner); }  
INCR_OWN  = { owner := owner + 1; }
```

# Ticketed Lock States

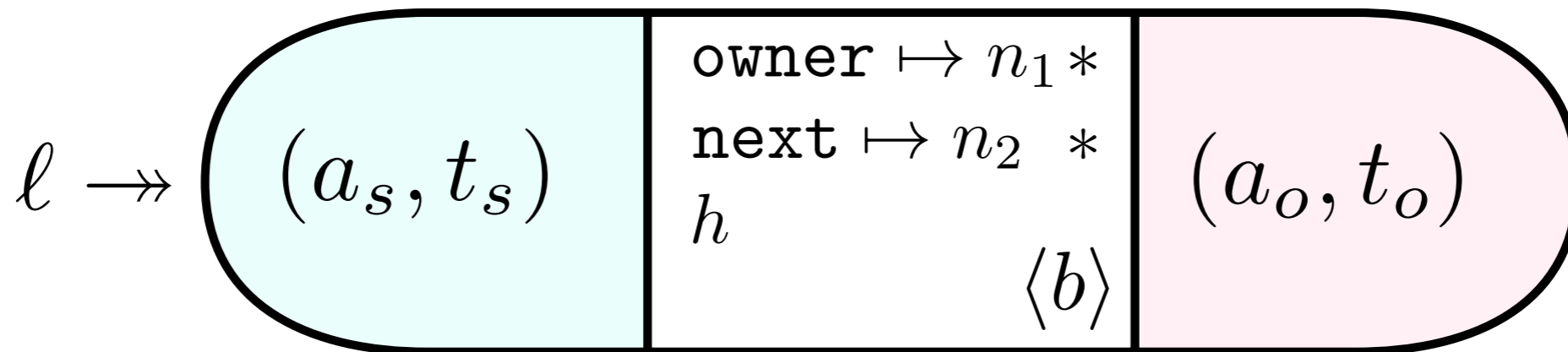


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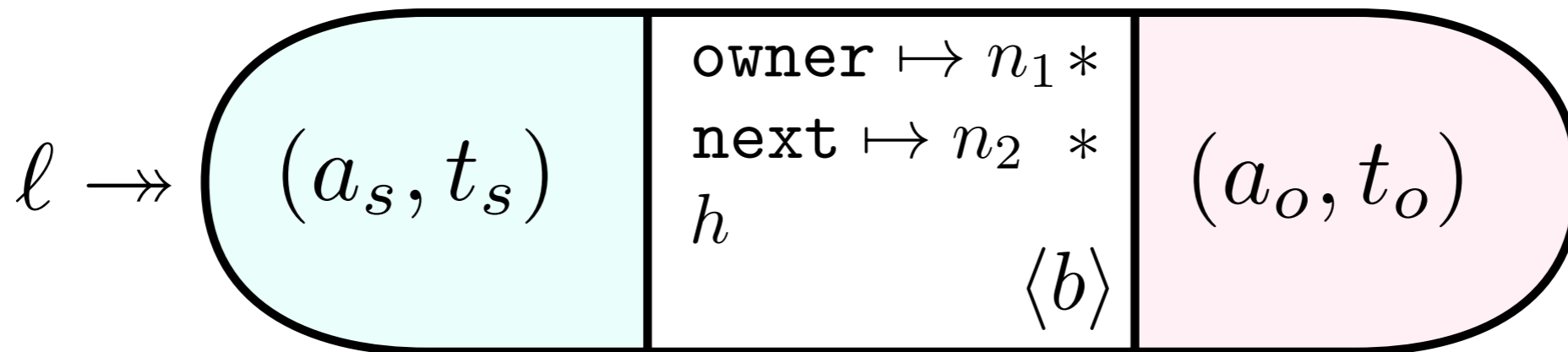
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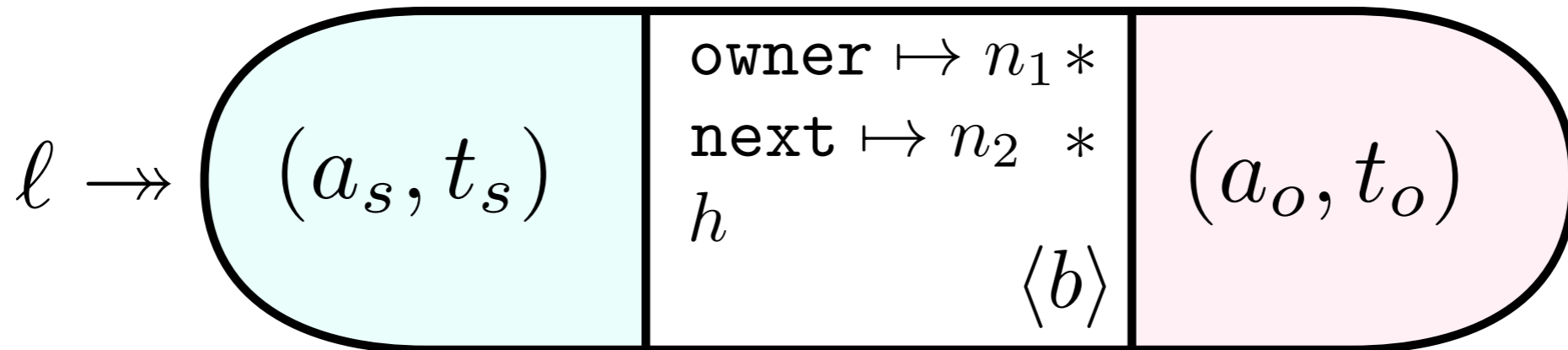
- $a_s, a_o$  - auxiliaries controlled by self/other
- $t_s$  - tickets, owned by self

# Ticketed Lock States



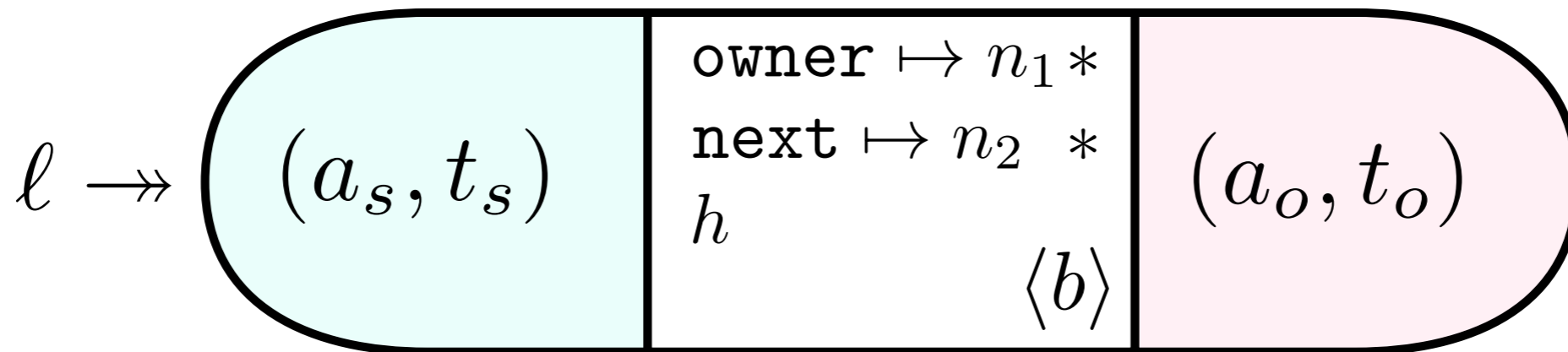
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- $a_s, a_o$  - auxiliaries controlled by self/other
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- $b$  - administrative flag to indicate locking

# Ticketed Lock States

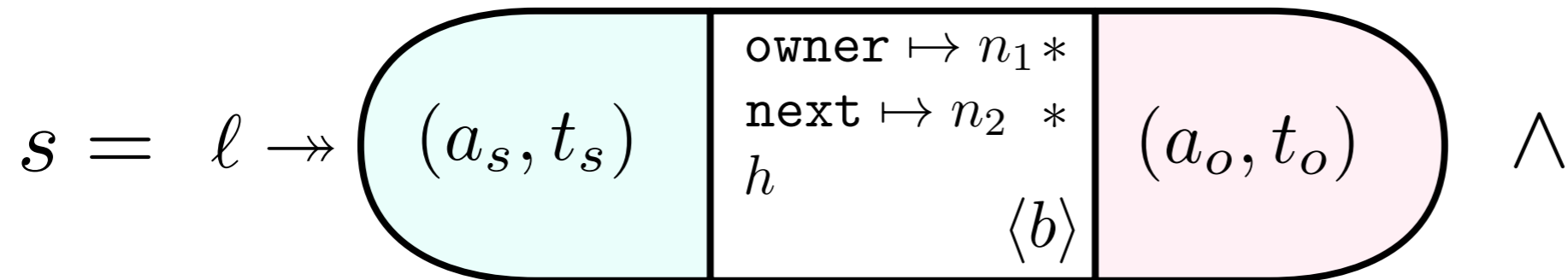


- $a_s, a_o$  - auxiliaries controlled by self/other
- $t_s$  - tickets, owned by *self*
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- $b$  - administrative flag to indicate locking
- $\ell$  - label to identify *this* particular instance of TLock concurrroid

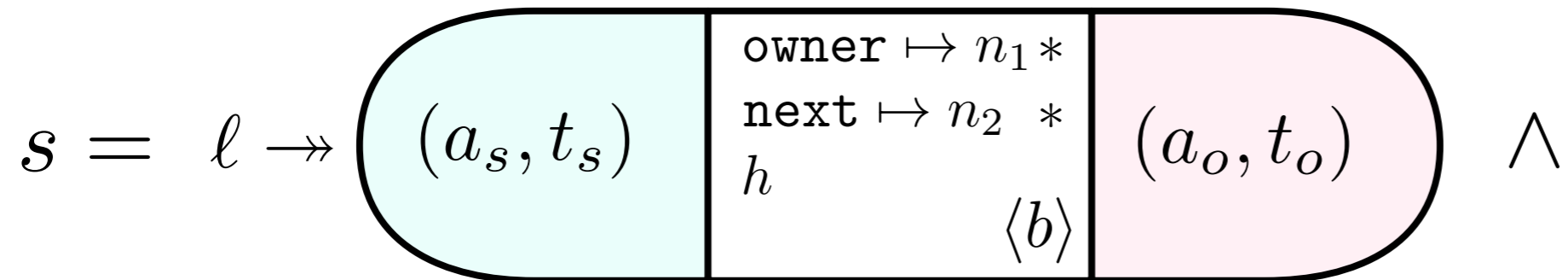
# Ticketed Lock Invariant



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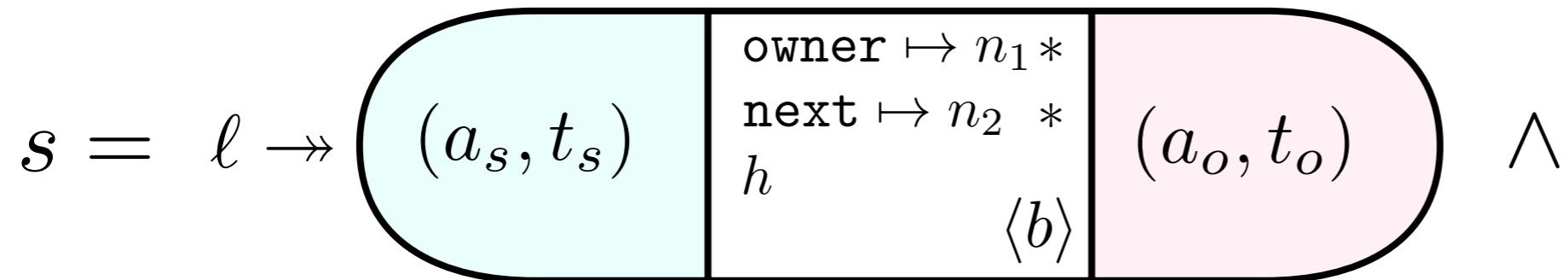


# Ticketed Lock Invariant

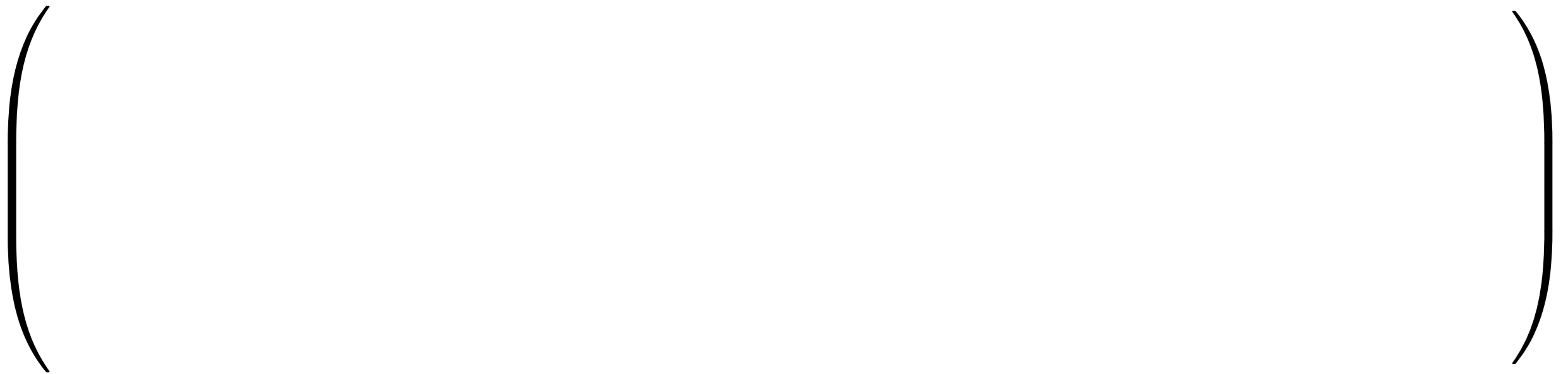


$t_s \oplus t_o = \{n \mid n_1 \leq n < n_2\}$   $\wedge$  **All dispensed tickets**

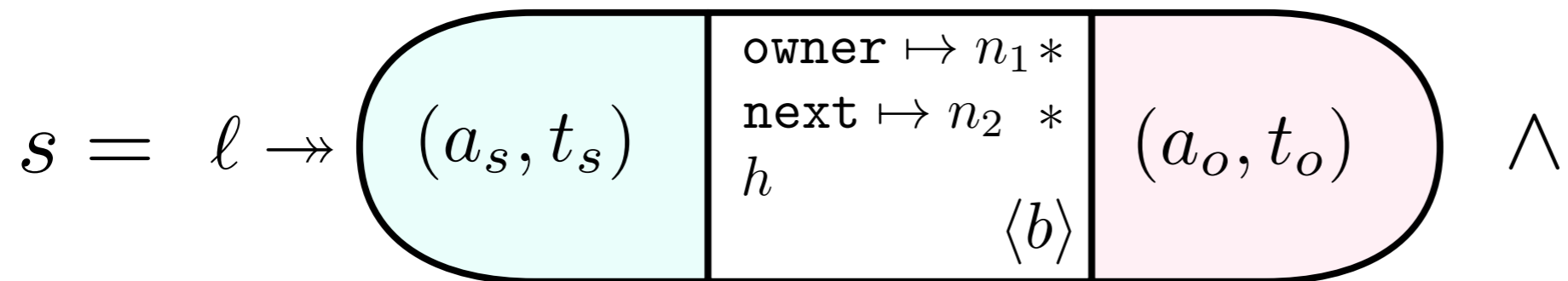
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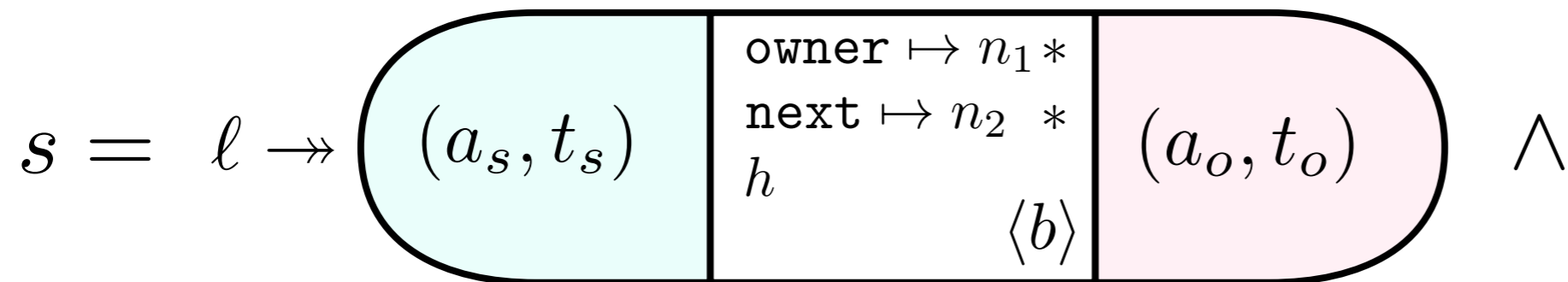
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$$t_s \oplus t_o = \{n \mid n_1 \leq n < n_2\} \wedge \text{All dispensed tickets}$$

$$\left( (n_1 \in (t_s \oplus t_o) \wedge b = \mathbf{true} \wedge h = \mathbf{emp}) \vee \text{Locked} \right)$$

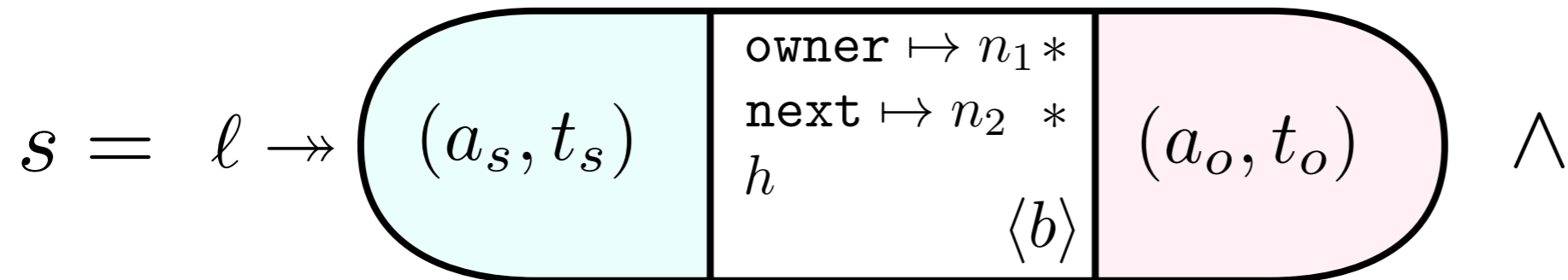
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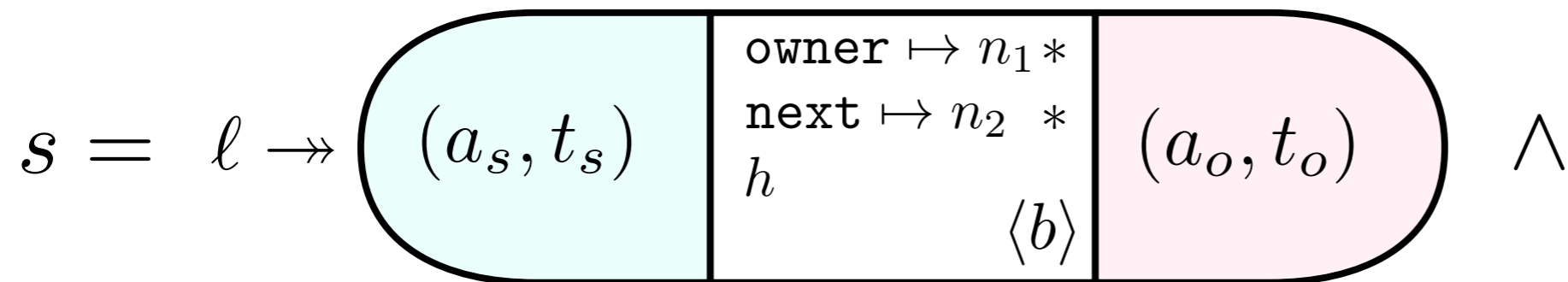


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Unlocked

# Transitions



# Internal Transitions

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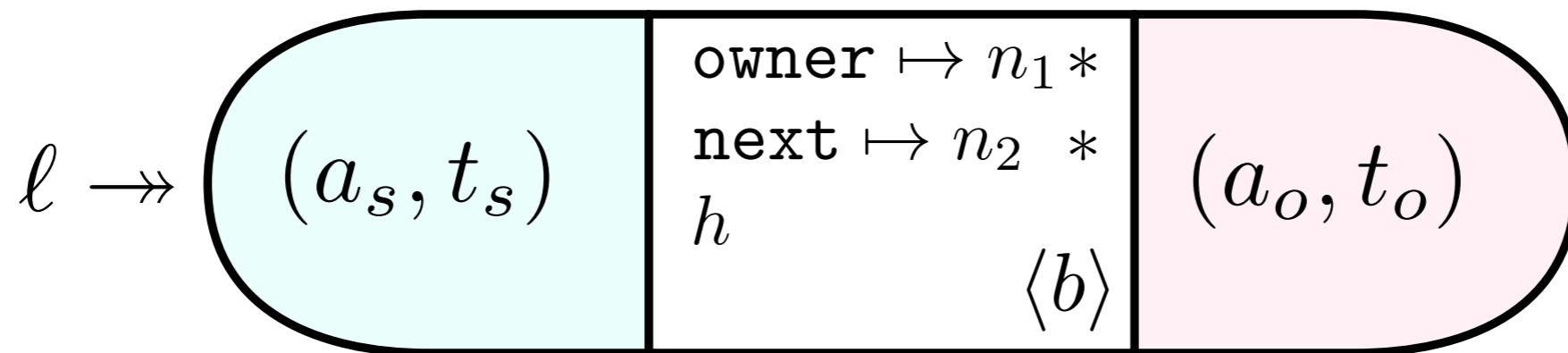
*Intuition:*

drawing a ticket from the dispenser

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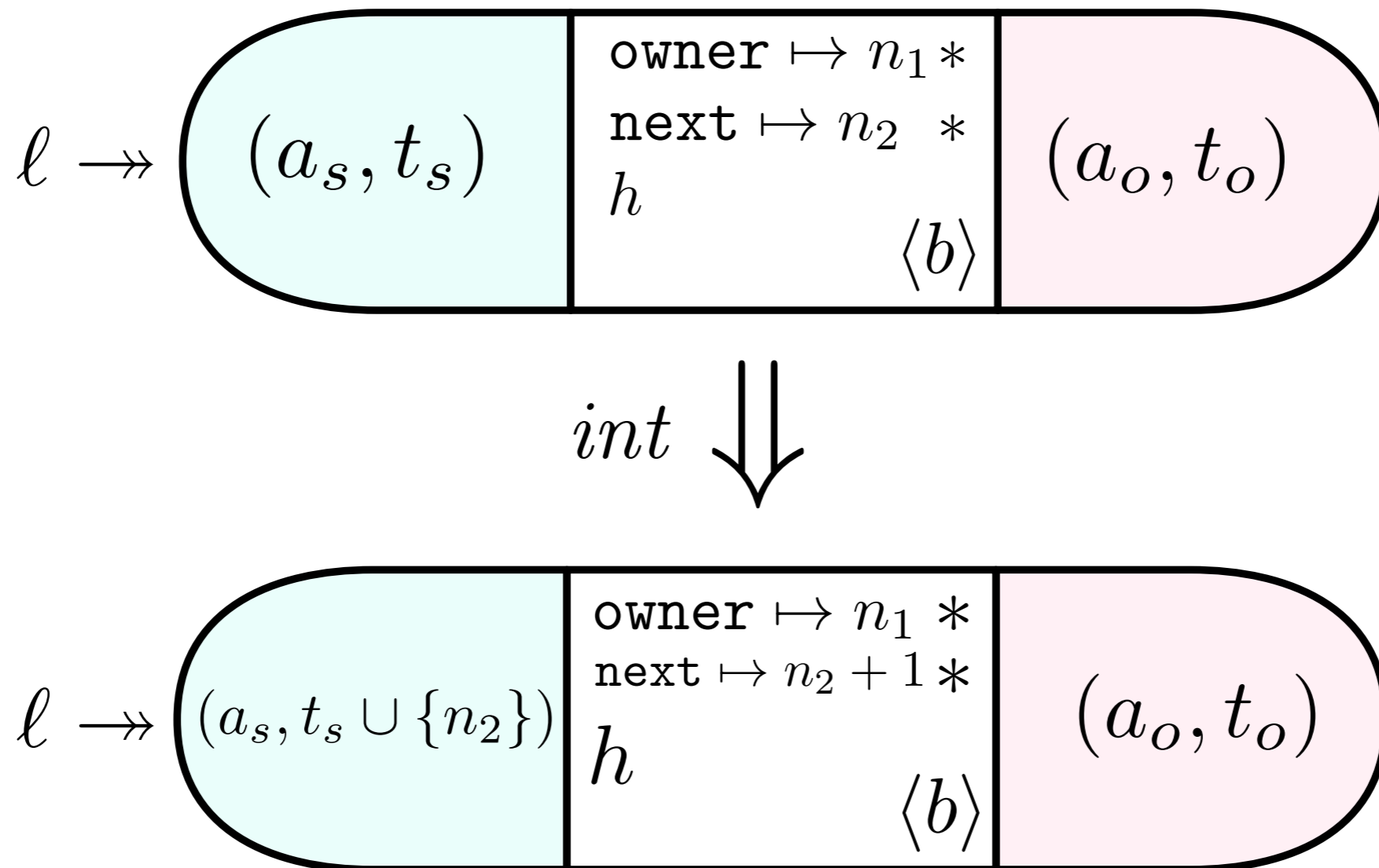
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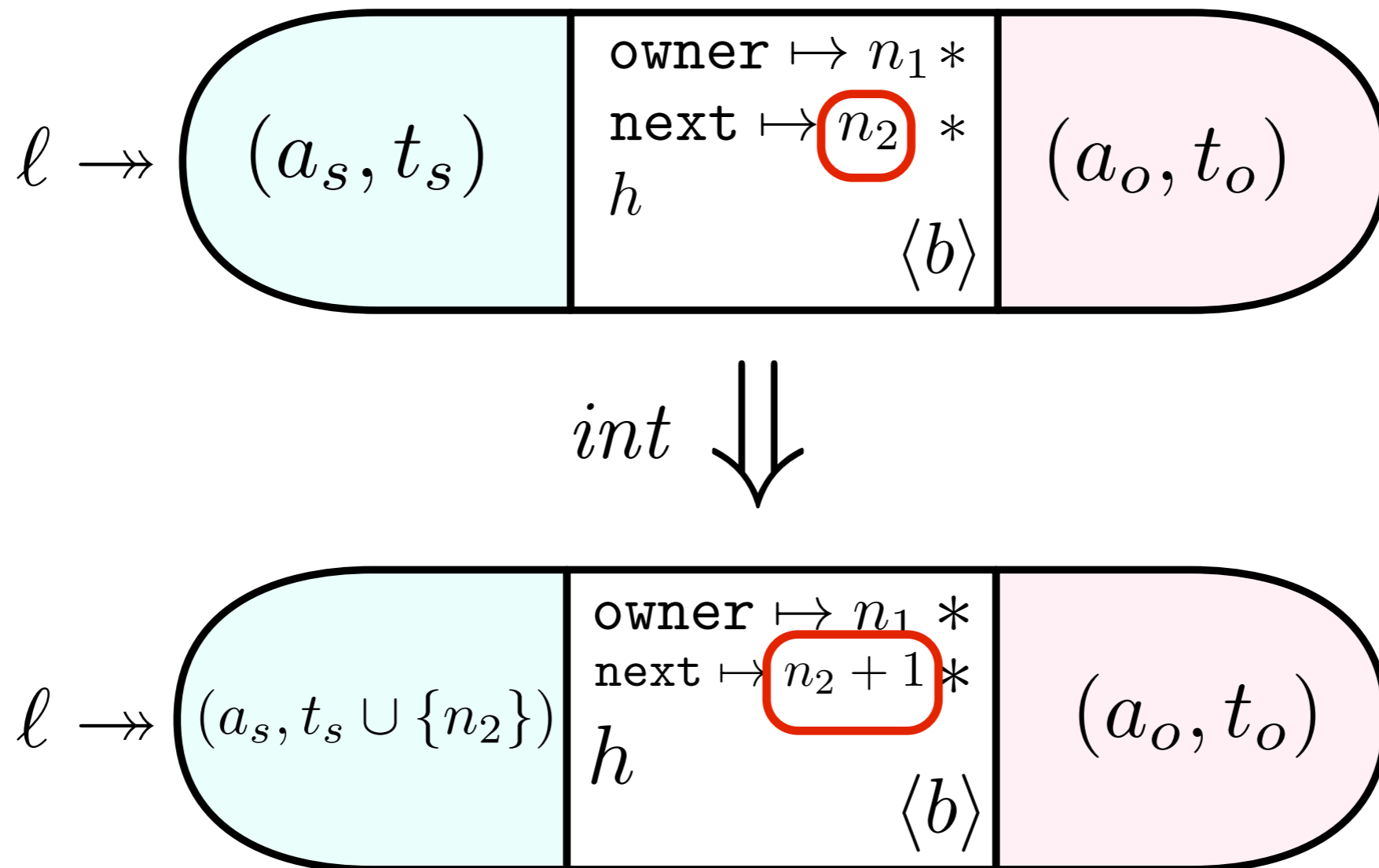
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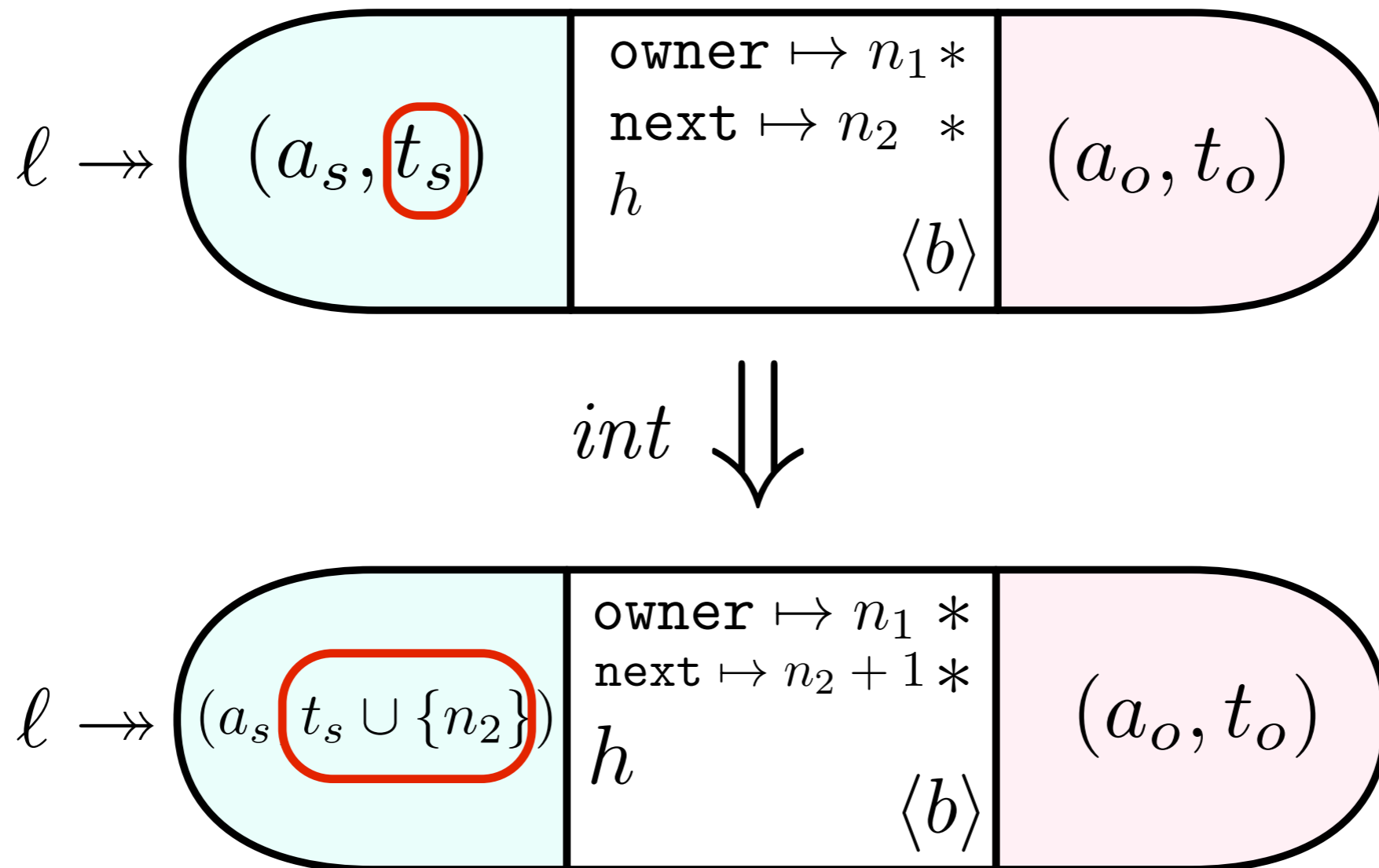
drawing a ticket from the dispenser



# Internal Transitions

Intuition:

drawing a ticket from the dispenser



**Communication**

# Intuition:

Channels with different polarity



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Channels with different polarity

# Implementation:

Acquire/Release transitions

(communication is via heap ownership transfer)

# Acquire Transitions

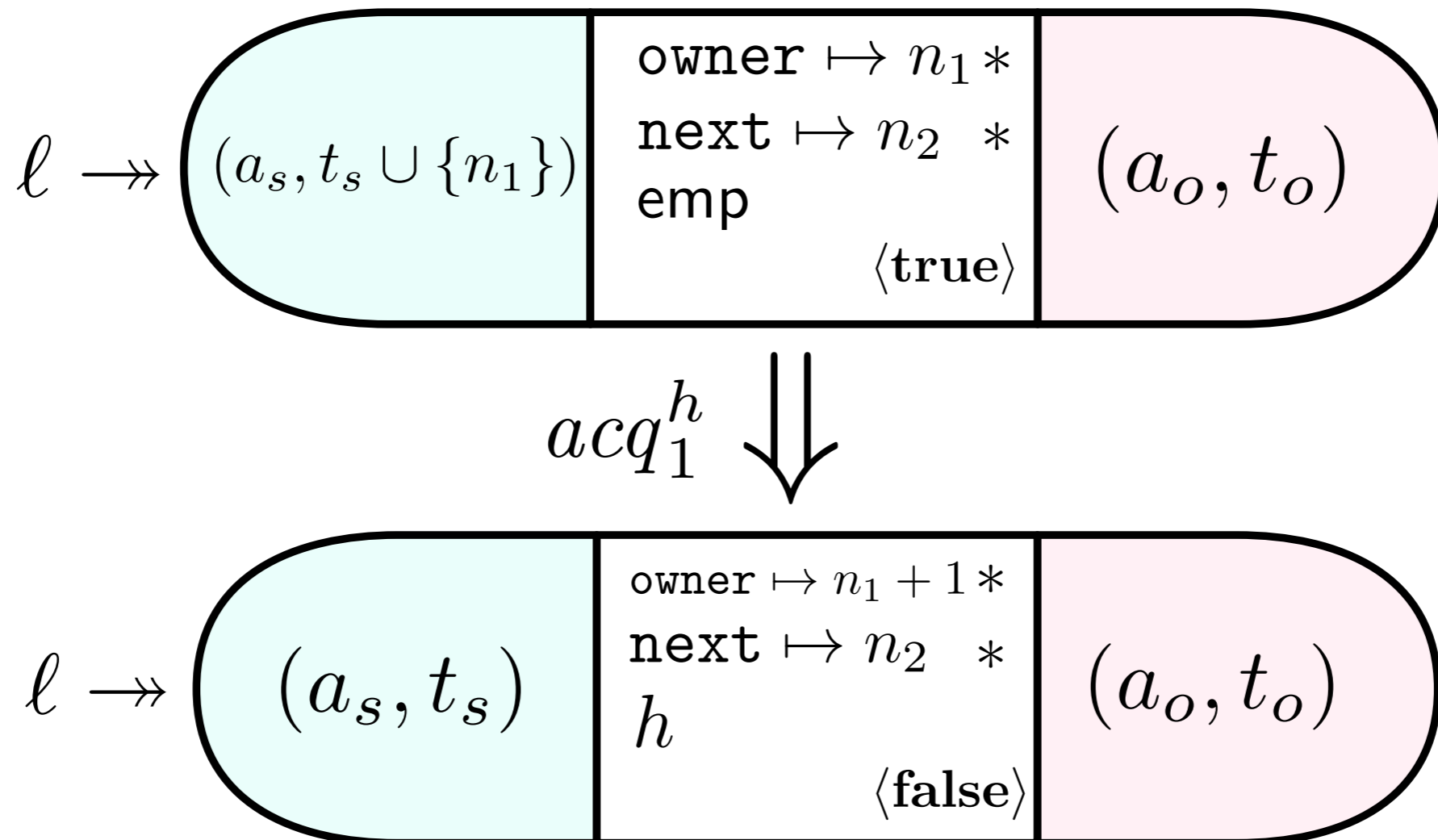
*Intuition:*

the lock obtains back ownership over the heap  
and increments the service counter (owner)

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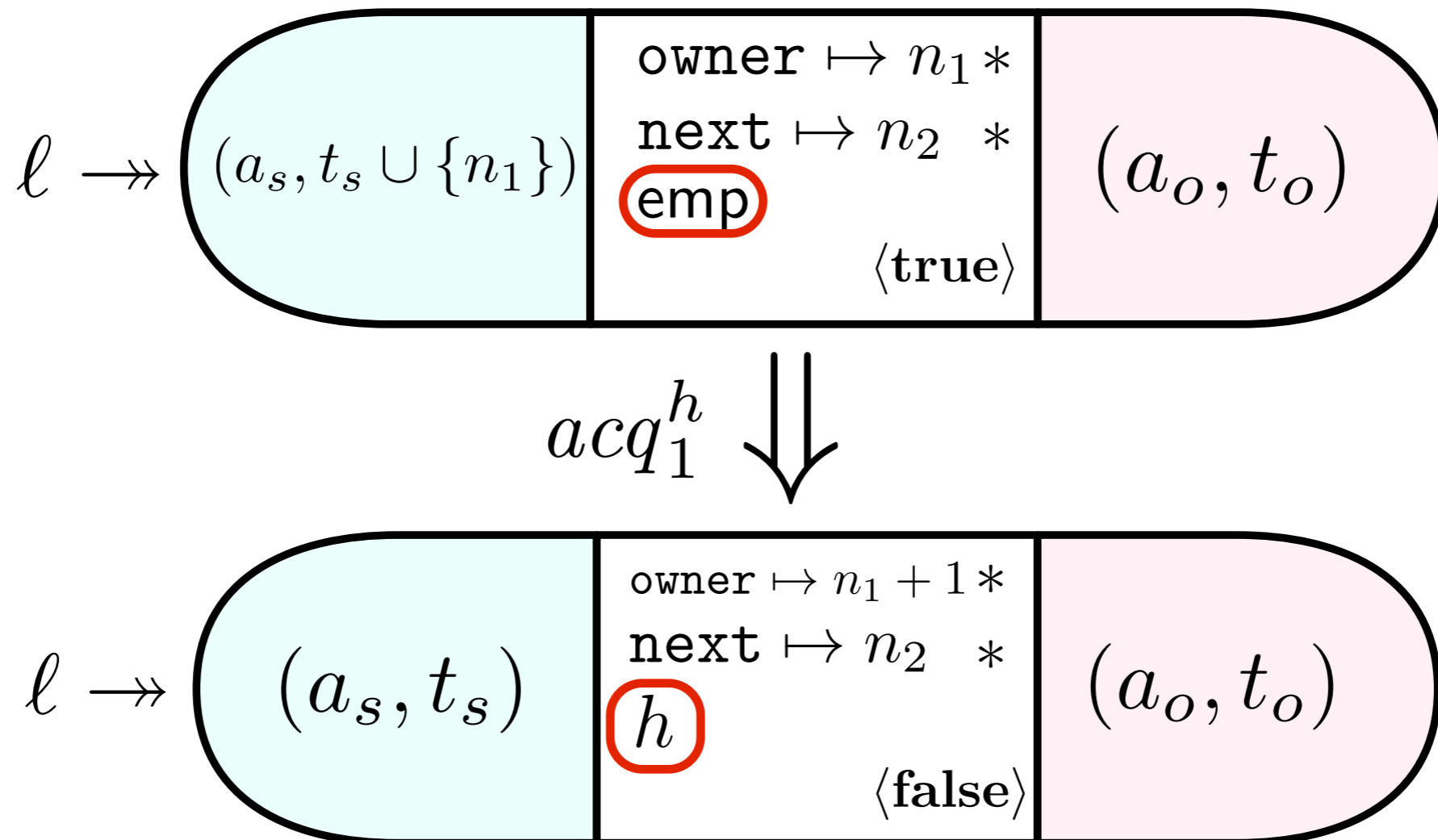
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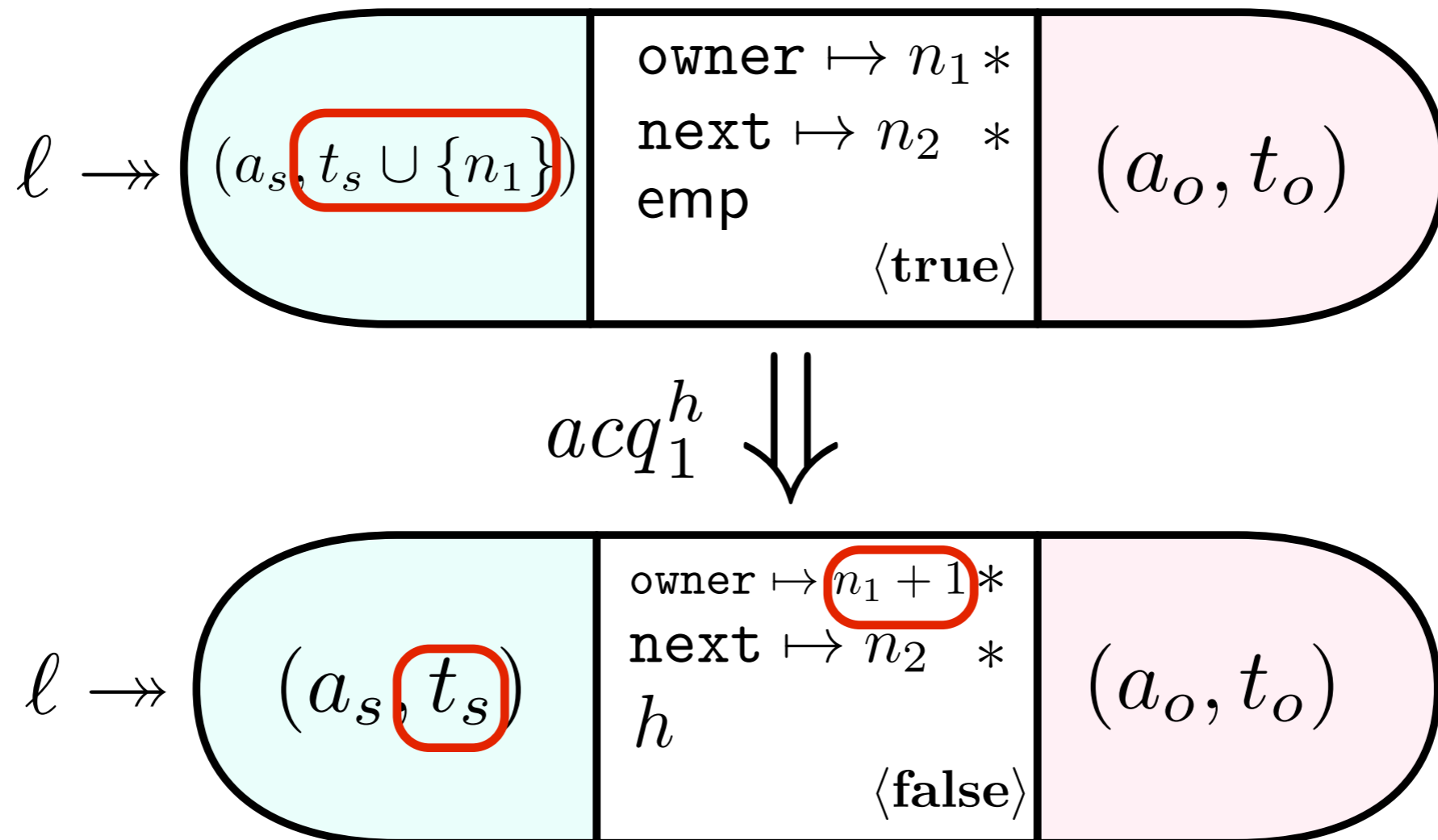
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# Release Transitions

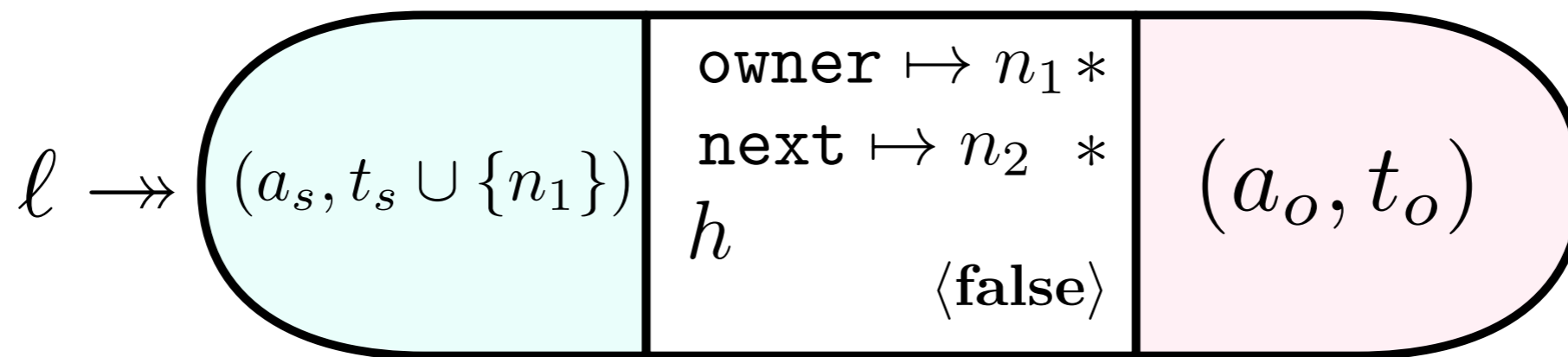
*Intuition:*

the lock gave up ownership over the heap

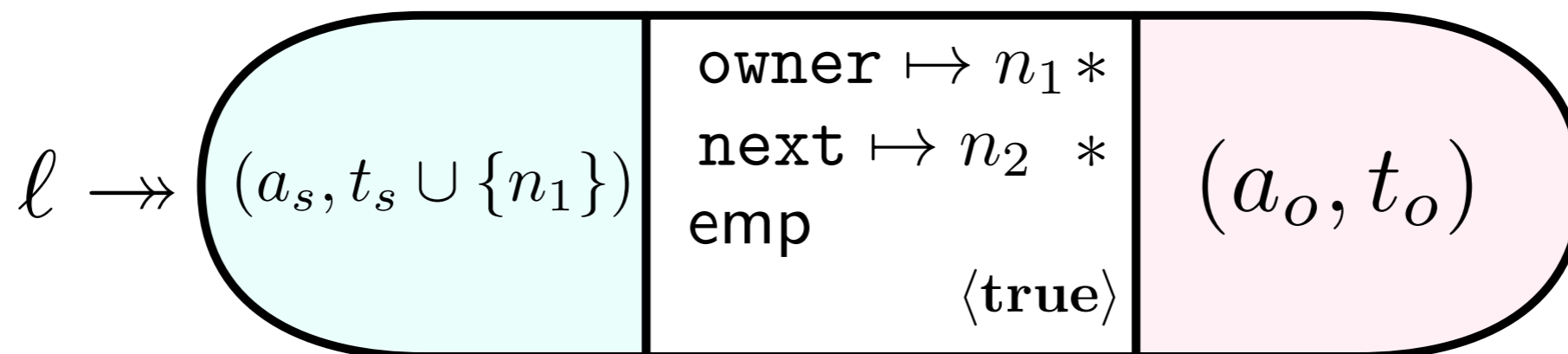
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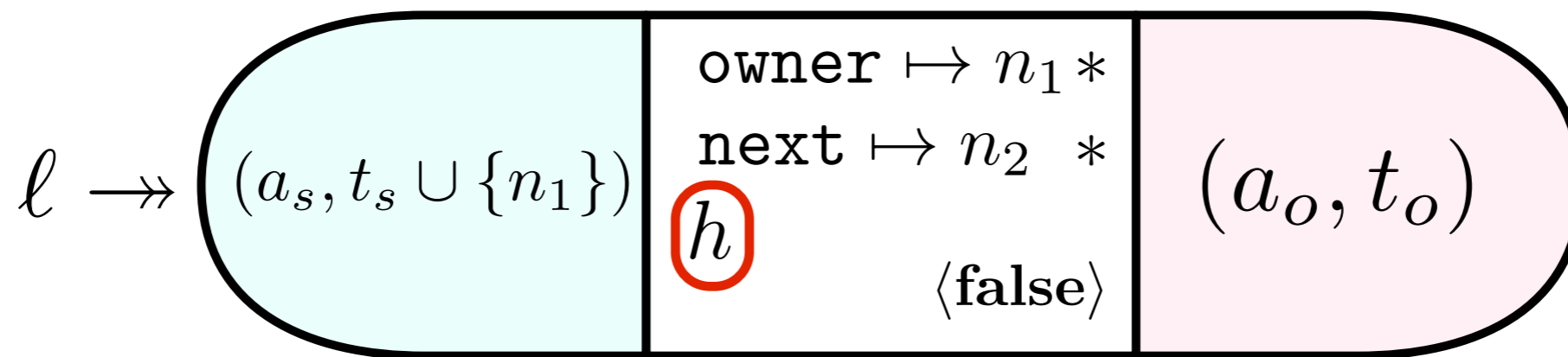
$\text{rel}_1^h \Downarrow$



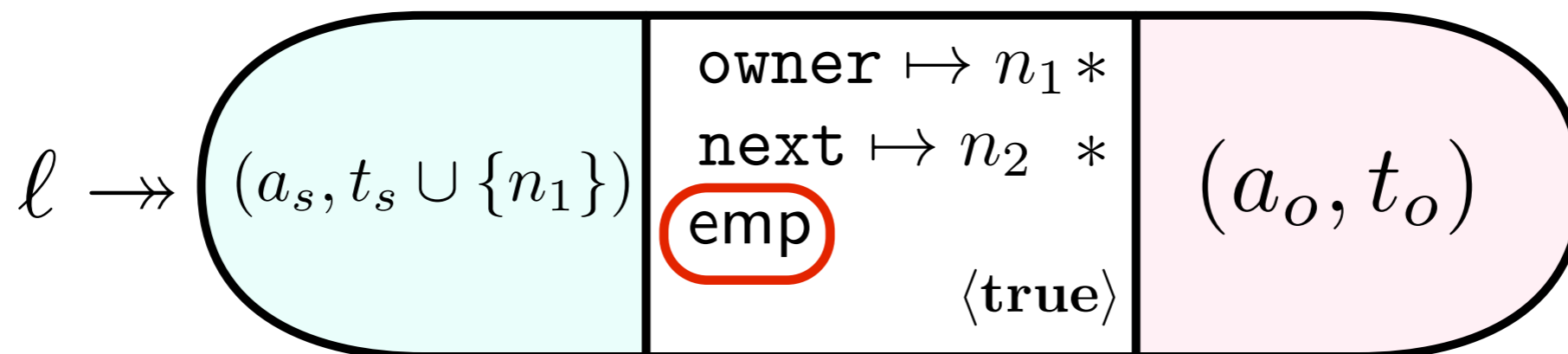
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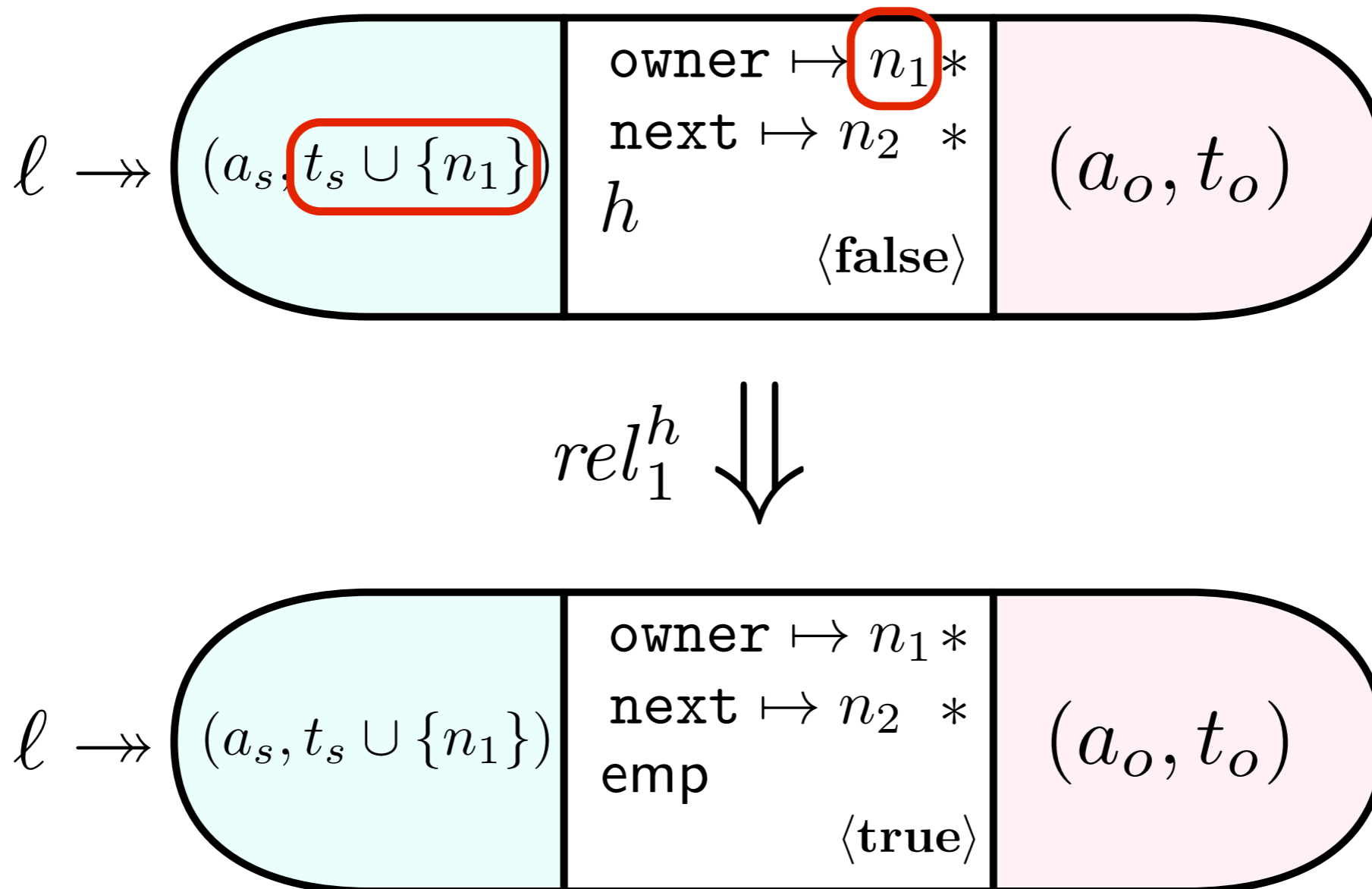




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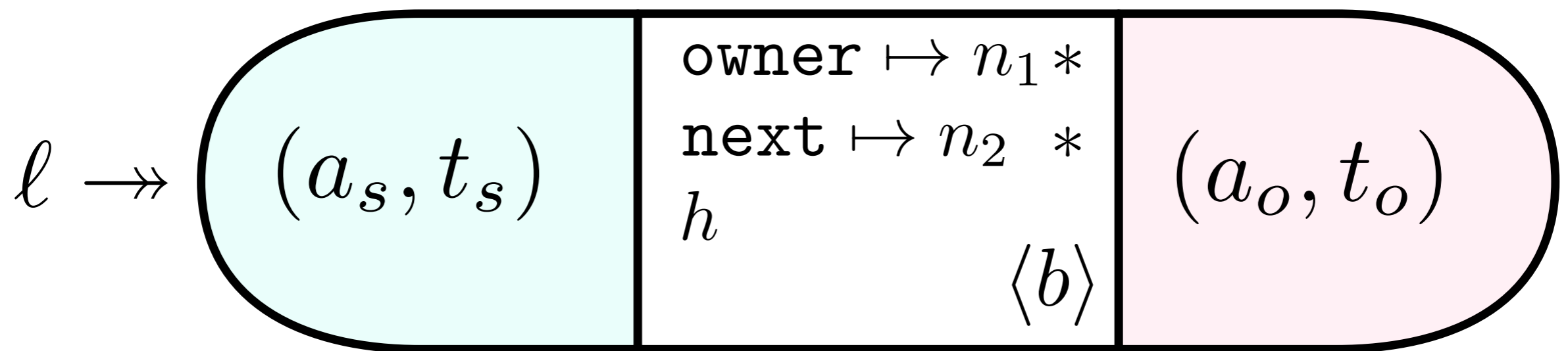


Transitions don't change the *other* part!

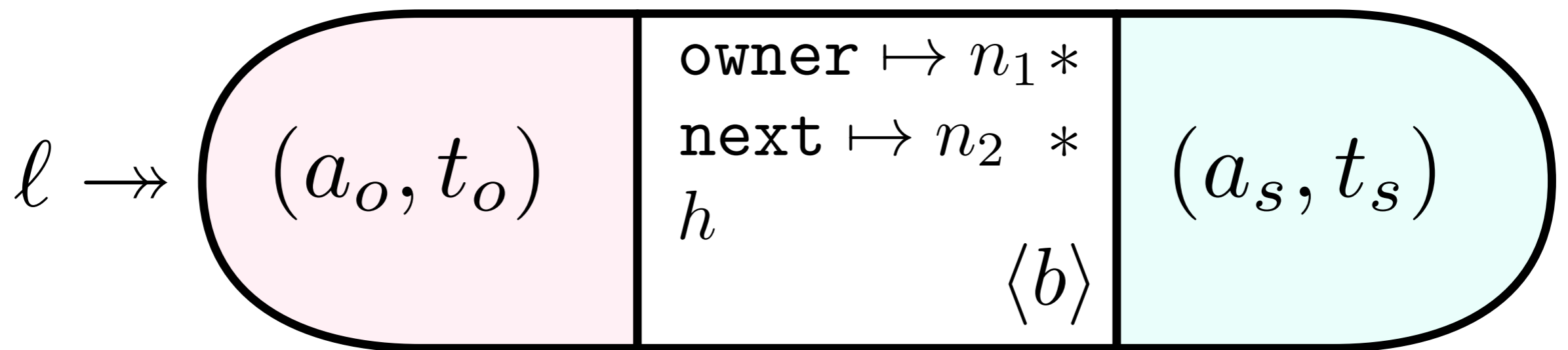
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Transitions = Guarantee

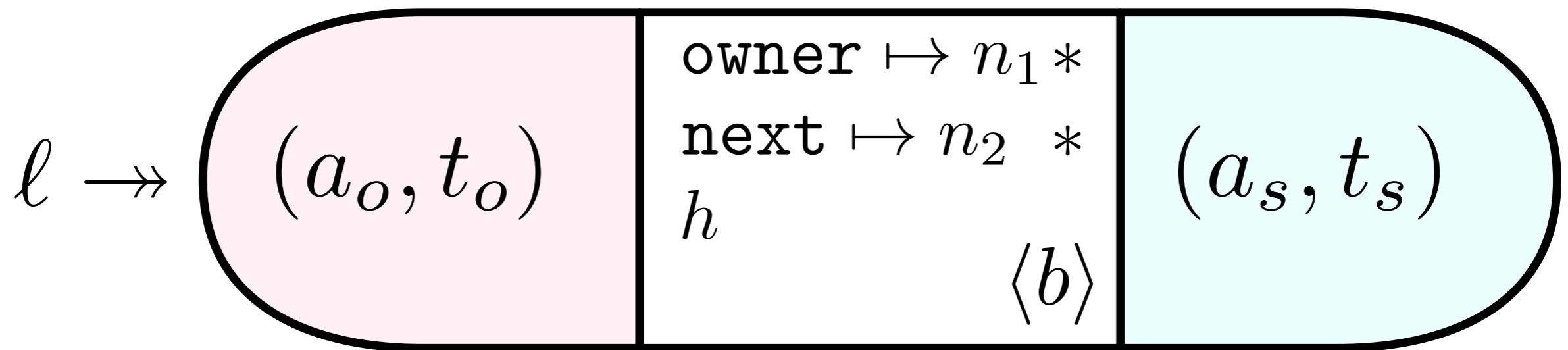
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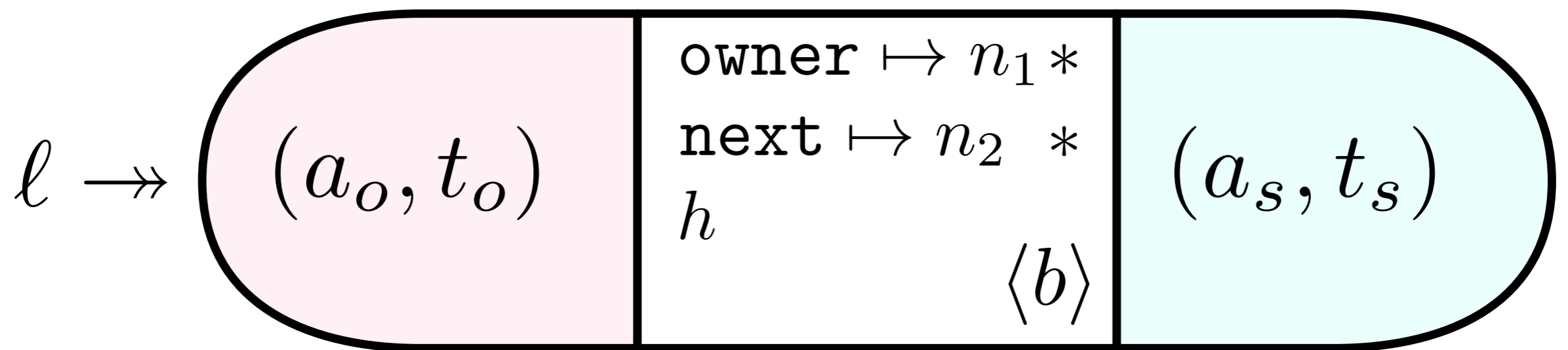


# Transposing the Concurroid



Transitions of transposed = Rely

# Transposing the Concurroid



Transitions of transposed = Rely

reminiscent to tokens by [Turon et al. \[POPL'13, ICFP'13\]](#)

# Composing Concurroids

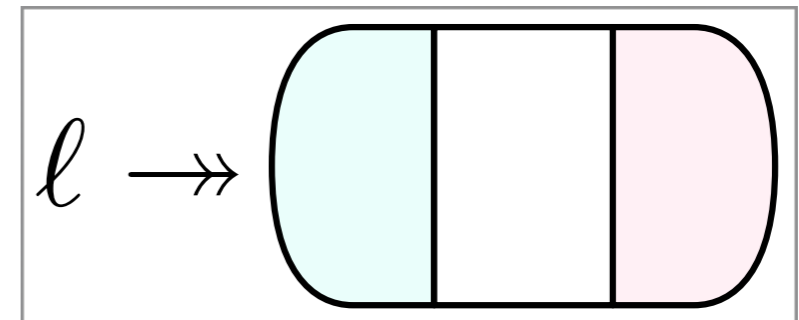
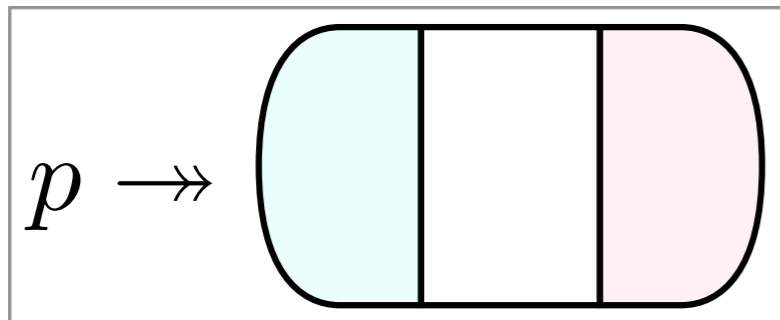


*Intuition:*

Connect communication channels with right polarity

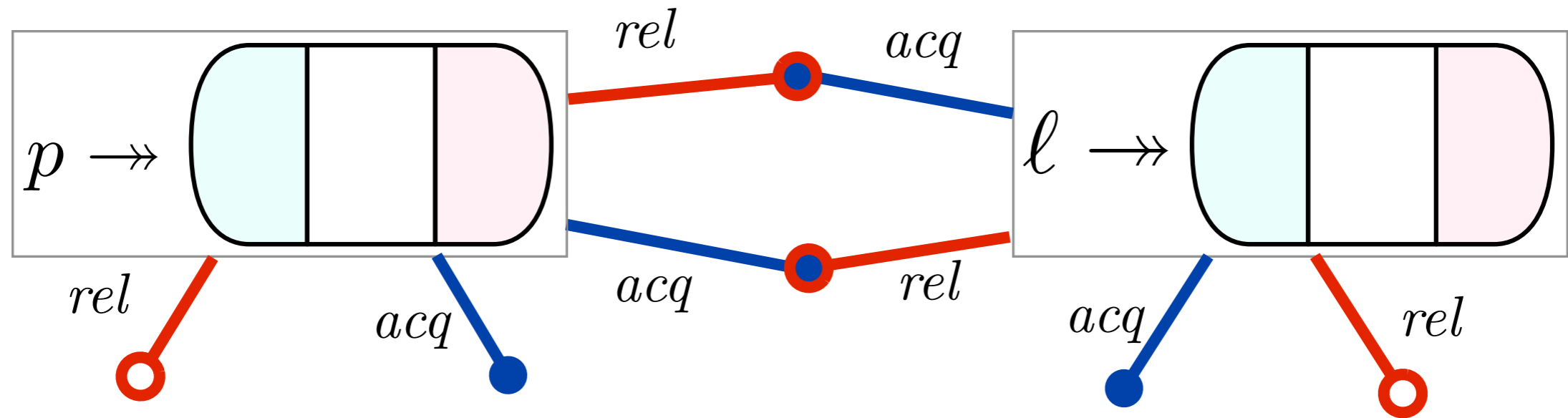
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Connect communication channels with right polarity



- Some channels might be left loose
- Same channels might be connected several times
- Some channels might be shut down

# Entanglement Operators

$\bowtie, \times, \bowtie, \times \dots$

Connect two concurroids by connecting some of their acquire/release transitions.

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$\bowtie, \times, \bowtie, \times \dots$

Connect two concurroids by connecting some of their acquire/release transitions.

Connected A/R transitions become *internal* for the entanglement.

# Useful Entanglement Operators

- × - “apart”, doesn’t connect channels, leaves all loose.
- ⊗ - connects all channels pair-wise, shuts channels of the right operand, leaves left one’s loose

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- × - “apart”, doesn’t connect channels, leaves all loose.
- ⋈ - connects all channels pair-wise, shuts channels of the right operand, leaves left one’s loose

Lemma:  $U \times (V_1 \times V_2) = (U \times V_1) \times V_2$

# Programming with Concurroids



**Transitions are not yet  
commands!**

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commands!

They only describe  
some *correct* behavior.

# Atomic Actions

- Defined as subsets of *internal transitions*
- Specify the result
- Operational meaning:  
READ, WRITE, SKIP and various RMW-commands
- *Synchronize* ownership transfer and manipulation with auxiliaries

# Recap: TLock Implementation

```
lock = {  
    x := DRAW;  
    while (!TRY(x)) SKIP;  
}
```

```
unlock = {  
    INCR_OWN;  
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$$\left( \begin{array}{l} s = p \rightarrow \left( \begin{array}{|c|c|c|} \hline h_s & & h_o \\ \hline \end{array} \oplus \ell \rightarrow \left( \begin{array}{|c|c|c|} \hline (a_s, t_s \cup \{n_1\}) & \begin{array}{l} \text{owner} \mapsto n'_1 * \\ \text{next} \mapsto n_2 * \\ h \\ \langle b \rangle \end{array} & (a_o, t_o) \\ \hline \end{array} \wedge \\ \\ \text{if } (n_1 = n'_1) \\ \text{then } \left( s' = p \rightarrow \left( \begin{array}{|c|c|c|} \hline h_s \oplus h & & h_o \\ \hline \end{array} \oplus \ell \rightarrow \left( \begin{array}{|c|c|c|} \hline (a_s, t_s \cup \{n_1\}) & \begin{array}{l} \text{owner} \mapsto n_1 * \\ \text{next} \mapsto n_2 * \\ \text{emp} \\ \langle \text{true} \rangle \end{array} & (a_o, t_o) \\ \hline \end{array} \wedge \\ \\ I(a_s \oplus a_o)h \wedge \text{res} = \text{true} \\ \\ \text{else } s' = s \wedge \text{res} = \text{false} \end{array} \right) \end{array} \right.$$



# TRY ( n<sub>1</sub> ) Action Specification

$$\text{TRY}(n_1)(s, s', \text{res}) \triangleq$$

$$s = p \rightarrow \left( \begin{array}{|c|c|} \hline h_s & h_o \\ \hline \end{array} \right) \oplus \ell \rightarrow \left( \begin{array}{|c|c|c|} \hline (a_s, t_s \cup \{n_1\}) & \begin{array}{l} \text{owner} \mapsto n'_1 * \\ \text{next} \mapsto n_2 * \\ h \end{array} & (a_o, t_o) \\ \hline \end{array} \right) \wedge$$

if ( n<sub>1</sub> = n'<sub>1</sub> )

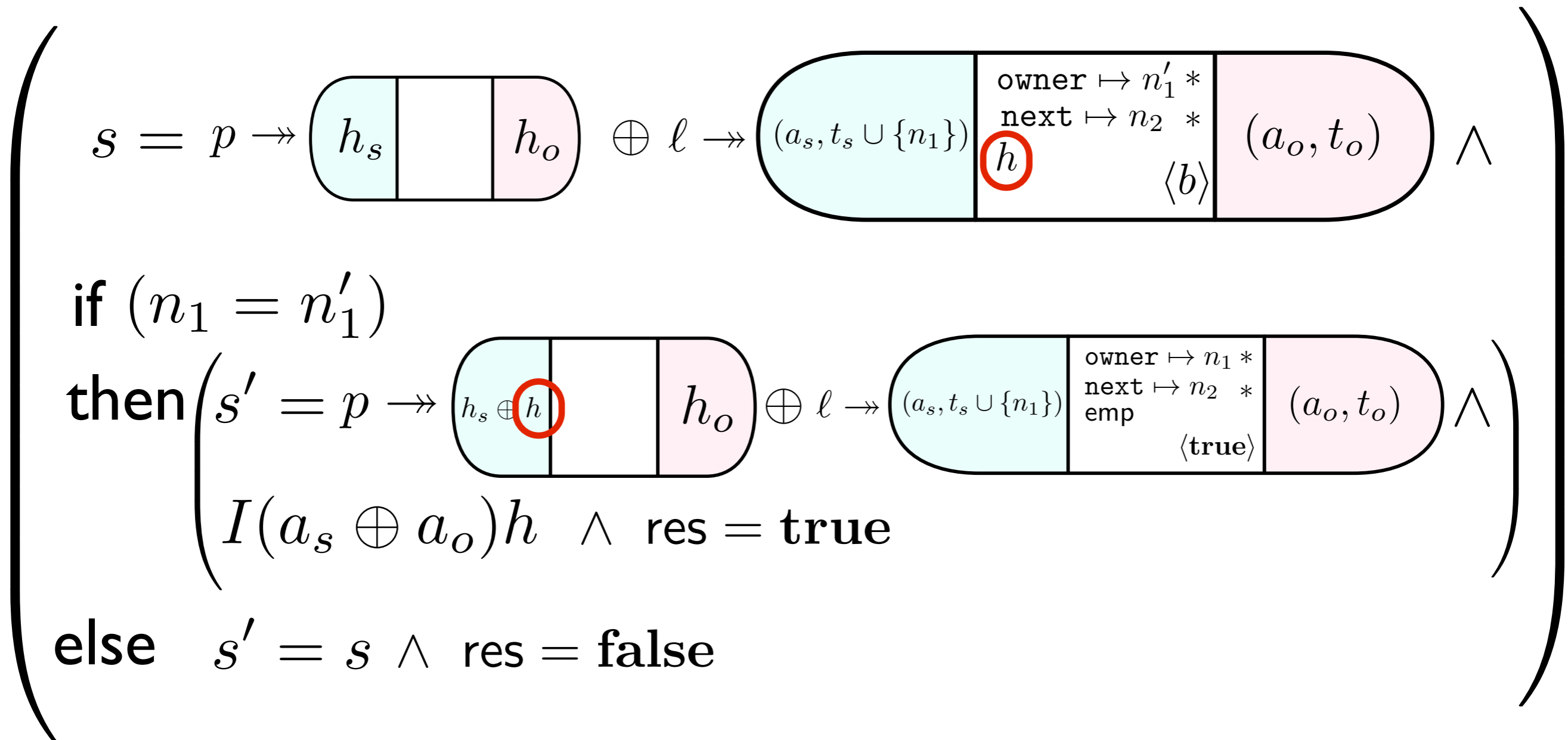
$$\text{then } \left( s' = p \rightarrow \left( \begin{array}{|c|c|} \hline h_s \oplus h & h_o \\ \hline \end{array} \right) \oplus \ell \rightarrow \left( \begin{array}{|c|c|c|} \hline (a_s, t_s \cup \{n_1\}) & \begin{array}{l} \text{owner} \mapsto n_1 * \\ \text{next} \mapsto n_2 * \\ \text{emp} \end{array} & (a_o, t_o) \\ \hline \end{array} \right) \wedge$$

$$I(a_s \oplus a_o)h \wedge \text{res} = \text{true}$$

else s' = s ∧ res = false

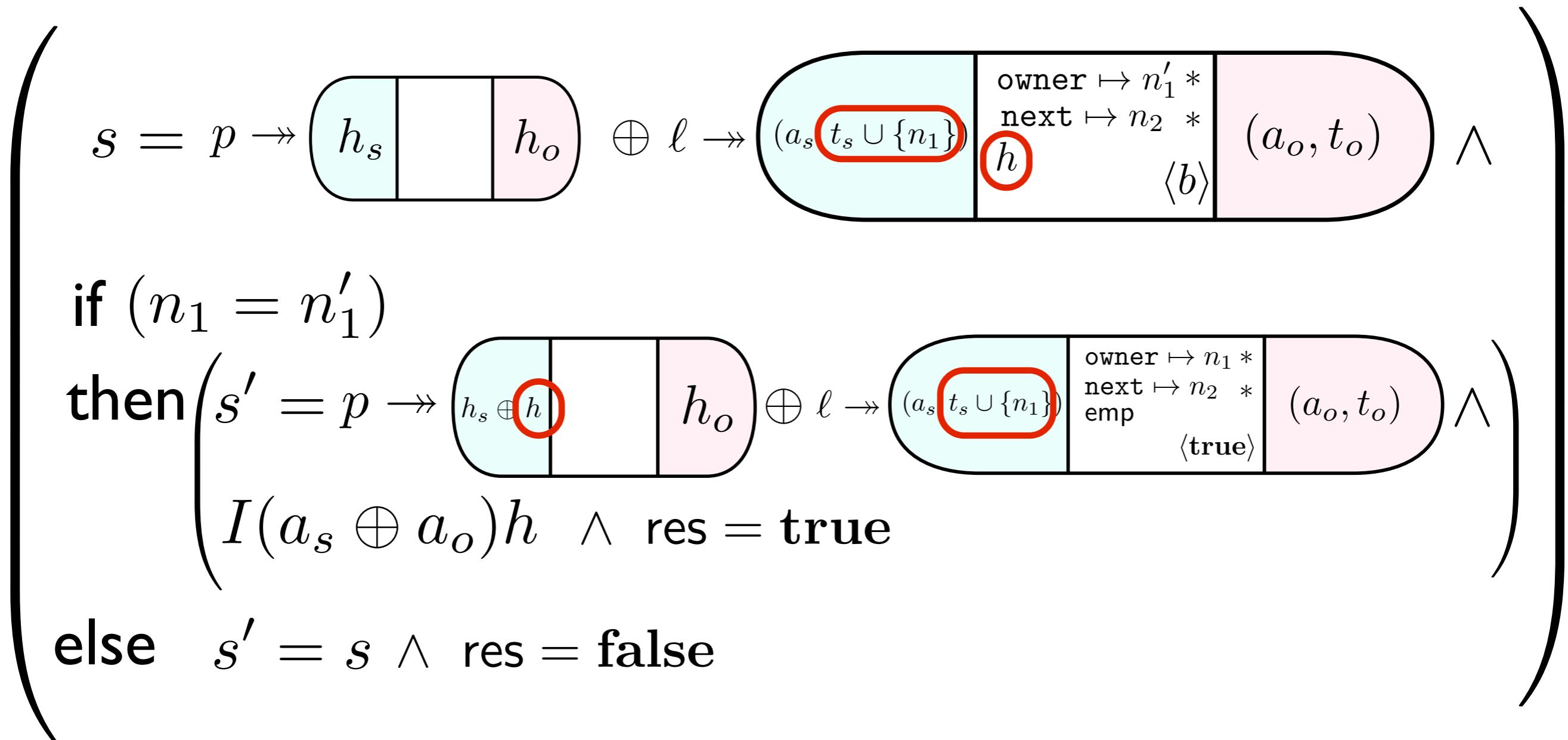
# TRY ( $n_1$ ) Action Specification

$$\text{TRY}(n_1)(s, s', \text{res}) \triangleq$$



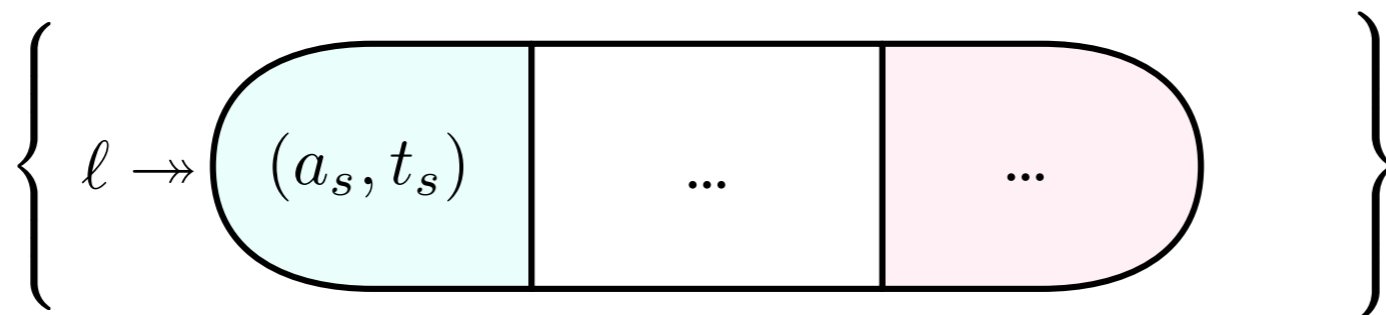
# TRY ( $n_1$ ) Action Specification

$$\text{TRY}(n_1)(s, s', \text{res}) \triangleq$$

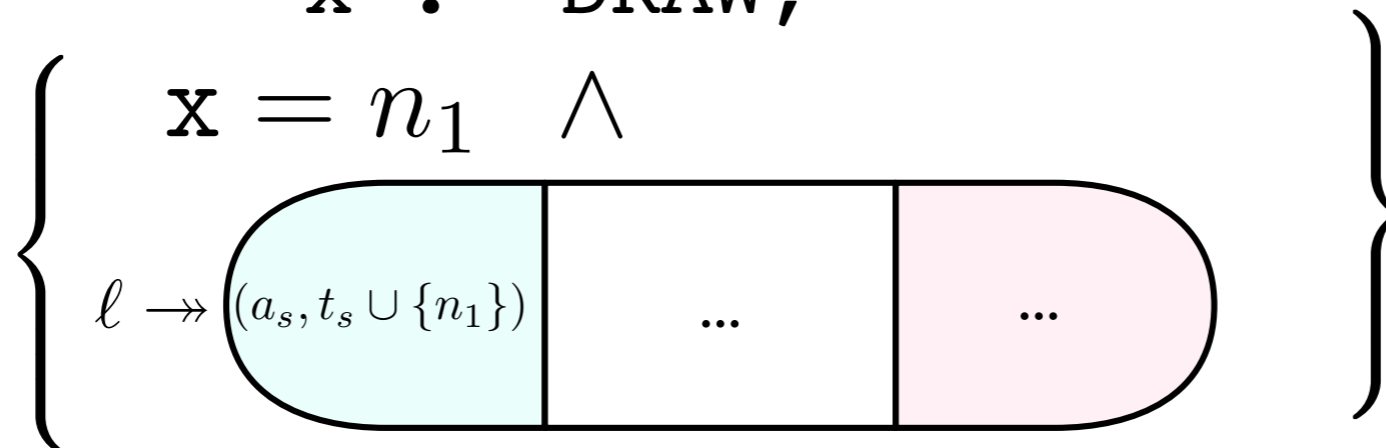


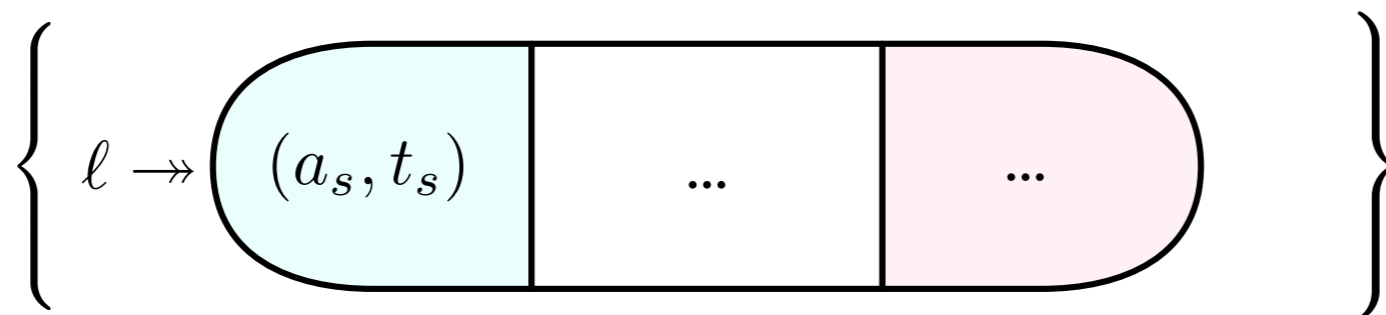
**What about modular  
reasoning?**

**x := DRAW;**

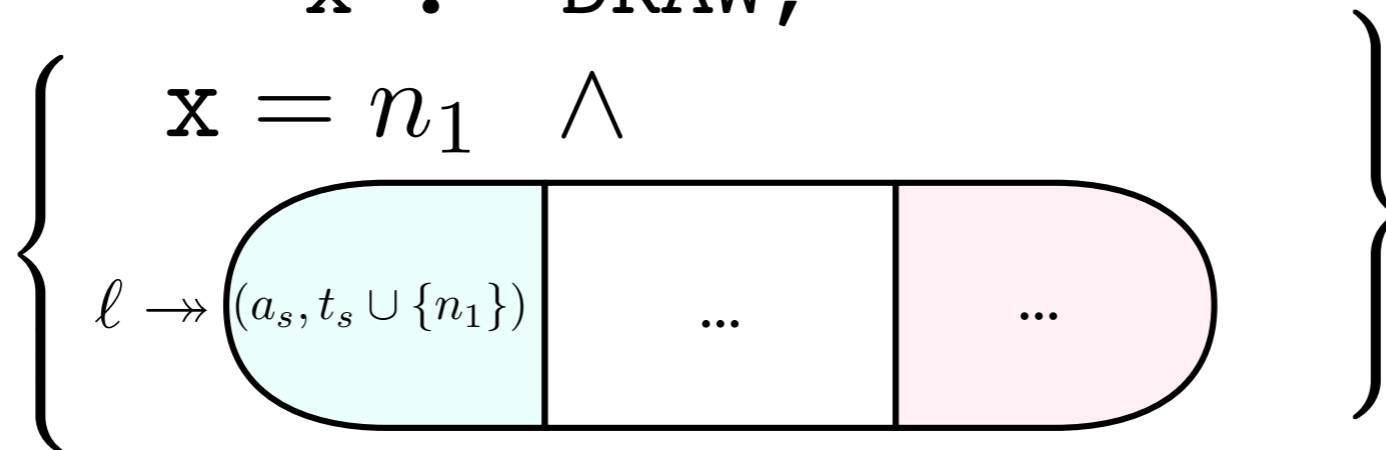


**$x := \text{DRAW};$**

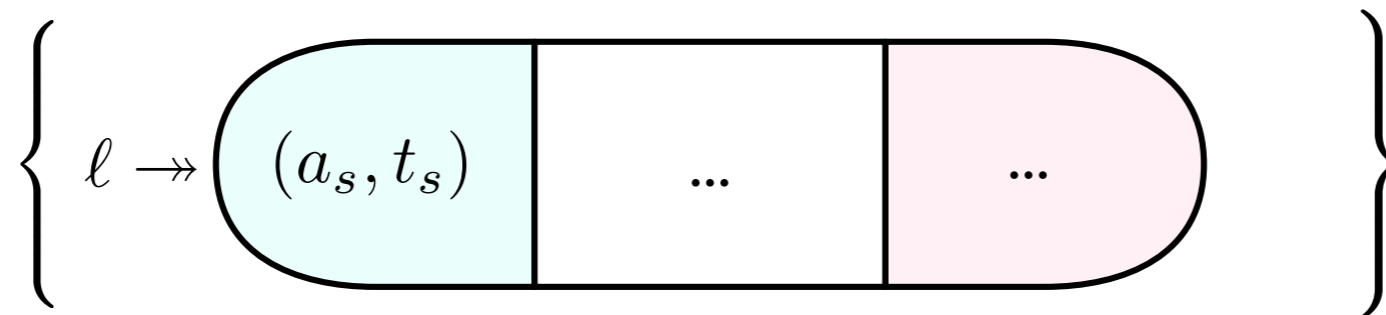




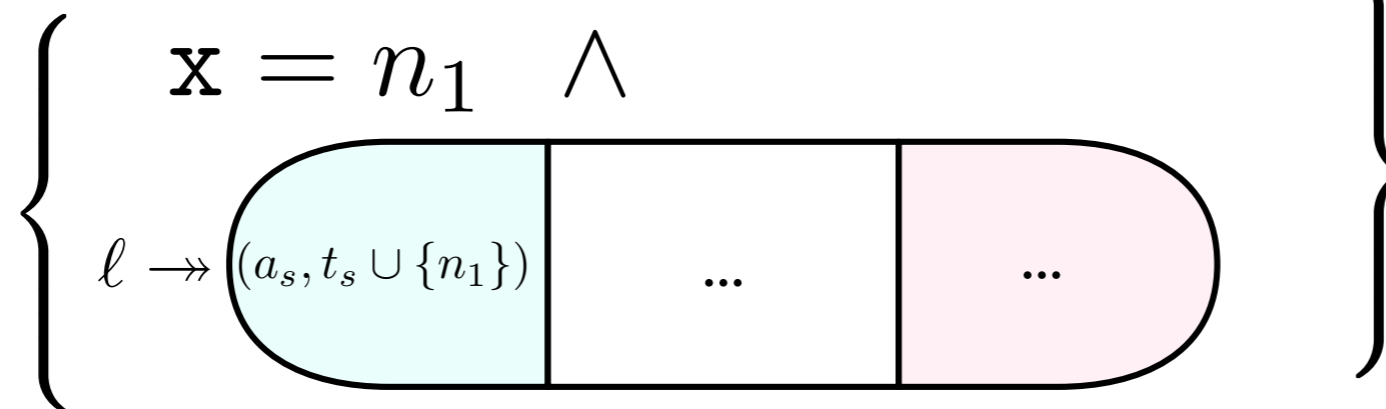
**$x := \text{DRAW};$**



lock = {



**x := DRAW;**

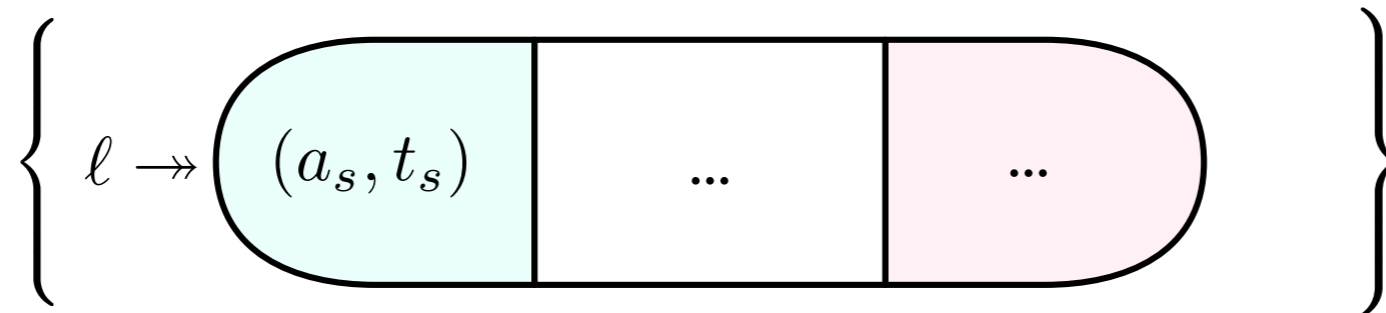


**while (!TRY(x)) SKIP;**

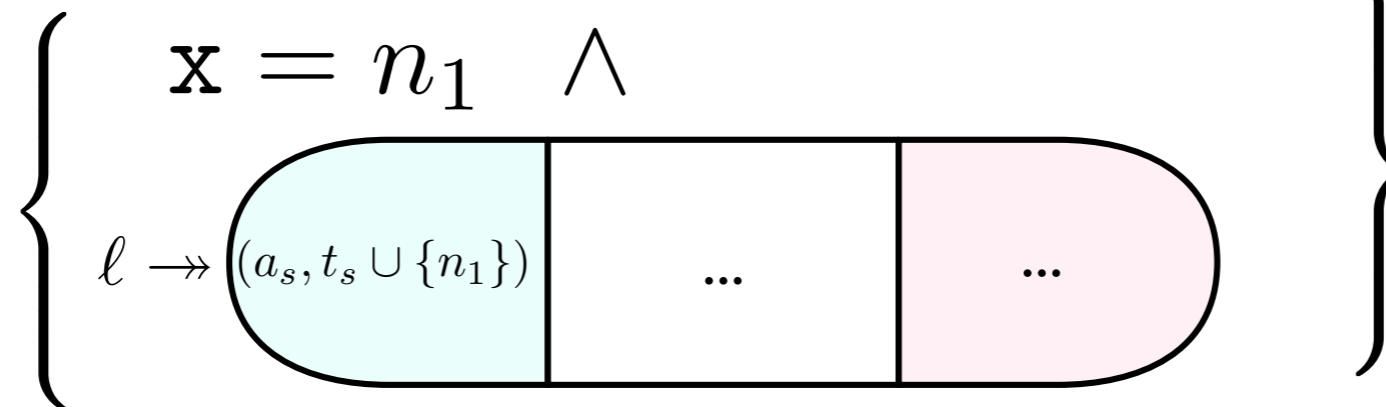
}



lock = {

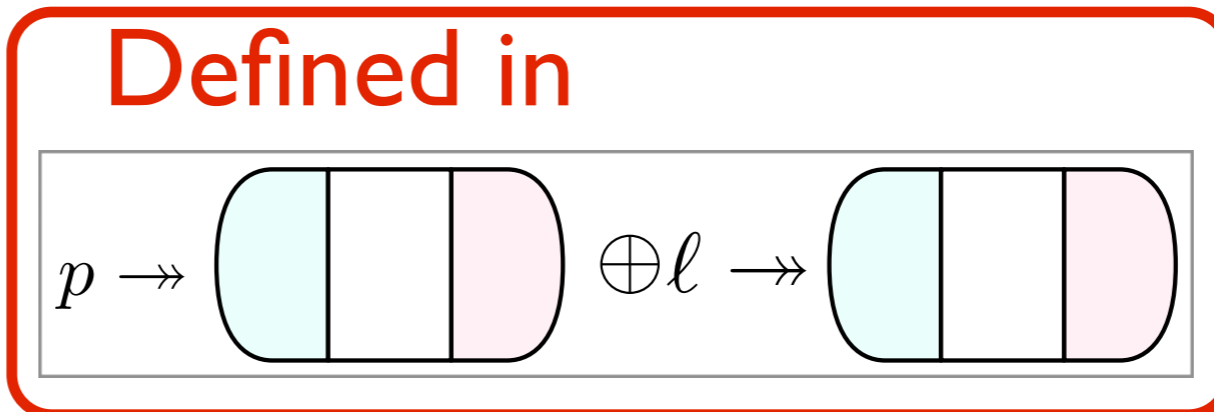


**x := DRAW;**



**while (!TRY(x)) SKIP;**

}



**Context Weakening!**

# Injection Rule

$$\frac{\{p\} C \{q\} @ U \quad r \text{ stable under } V}{\{p * r\} \text{ inject}_V C \{q * r\} @ U \blacktriangleright V} \text{ INJECT}$$

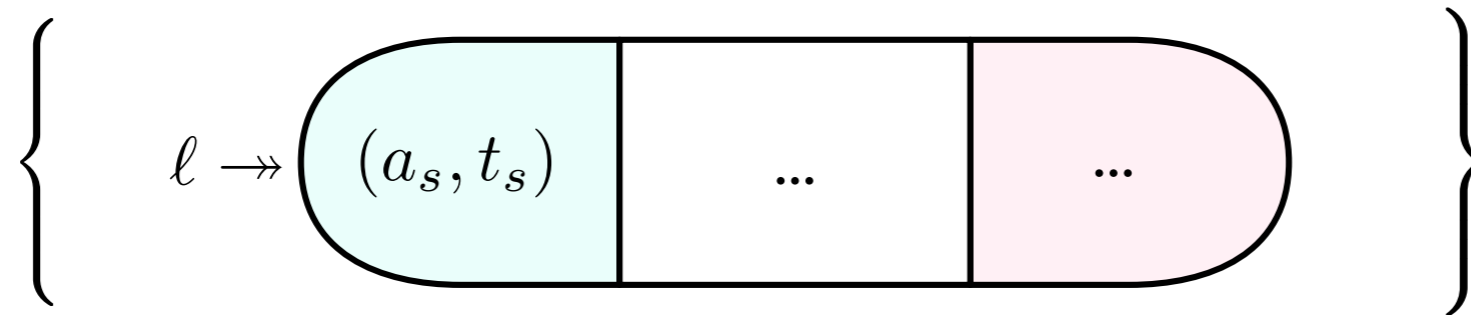
where  $\blacktriangleright = \bowtie, \times, \blacktriangleright, \times \dots$

# Injection Rule

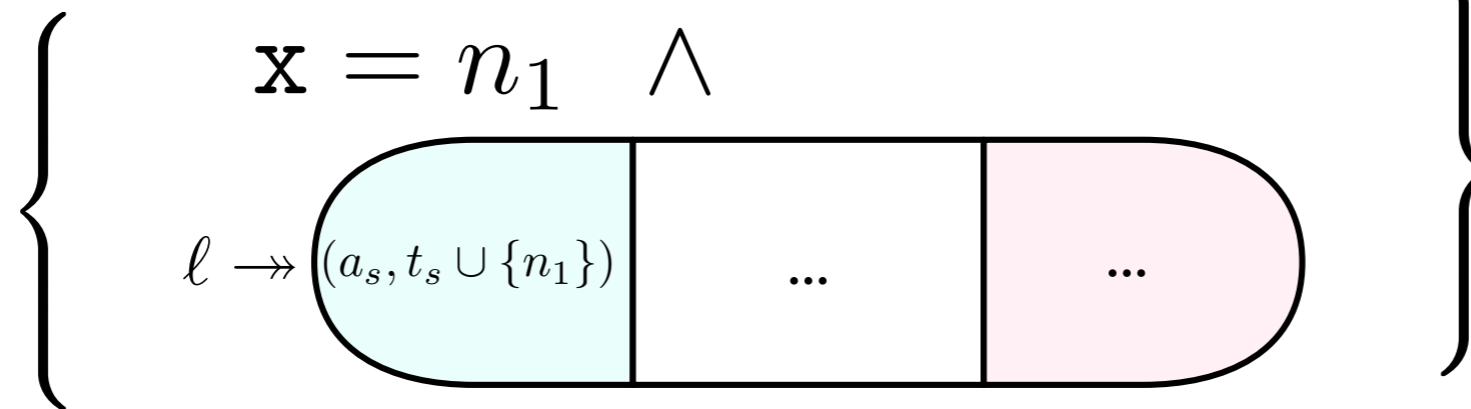
$$\frac{\{p\} C \{q\} @ U \quad r \text{ stable under } V}{\{p * r\} \text{ inject}_V C \{q * r\} @ U \blacktriangleright V} \text{ INJECT}$$

where  $\blacktriangleright = \bowtie, \times, \blacktriangleright, \times \dots$

lock = {



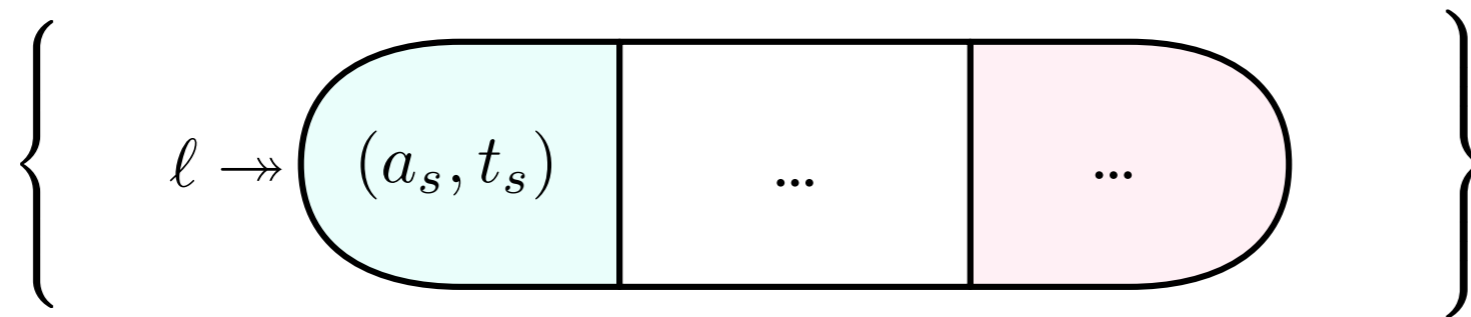
**x := DRAW ;**



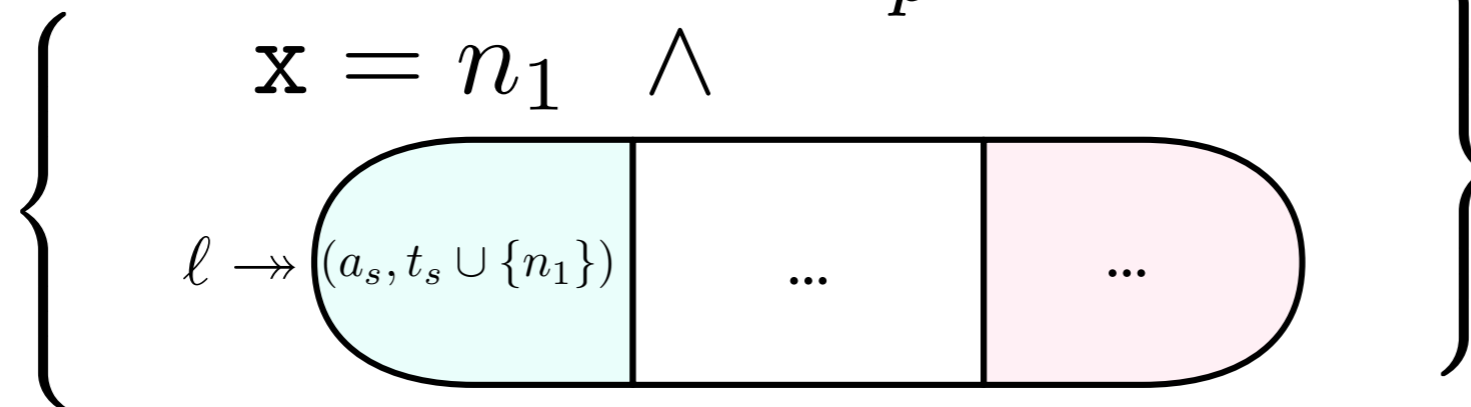
**while (!TRY(x)) SKIP;**

}

lock = {



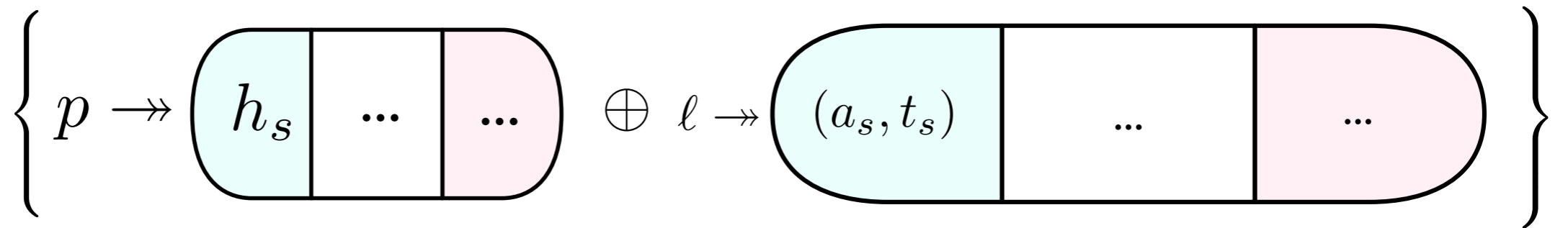
$\mathbf{x} := inject_p(DRAW);$



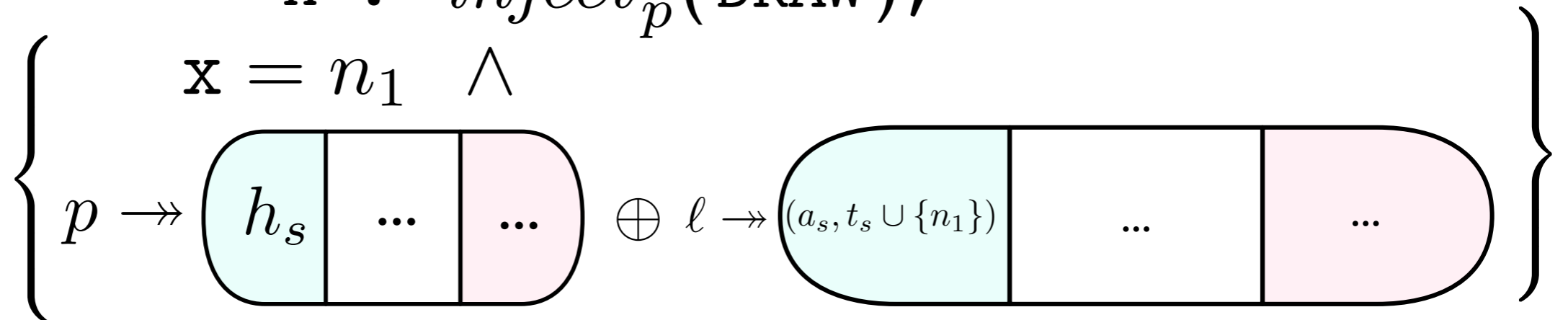
$\mathbf{while} (!TRY(\mathbf{x})) \mathbf{SKIP};$

}

lock = {



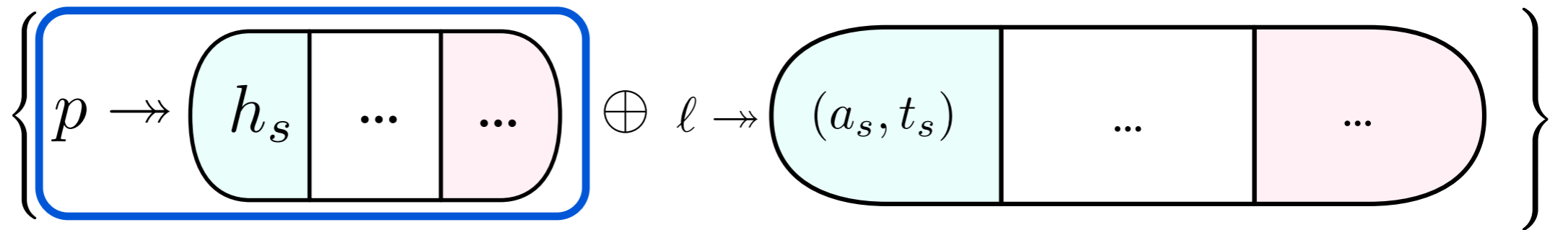
$\mathbf{x} := \mathit{inject}_p(\text{DRAW});$



$\text{while } (!\text{TRY}(\mathbf{x})) \text{ SKIP};$

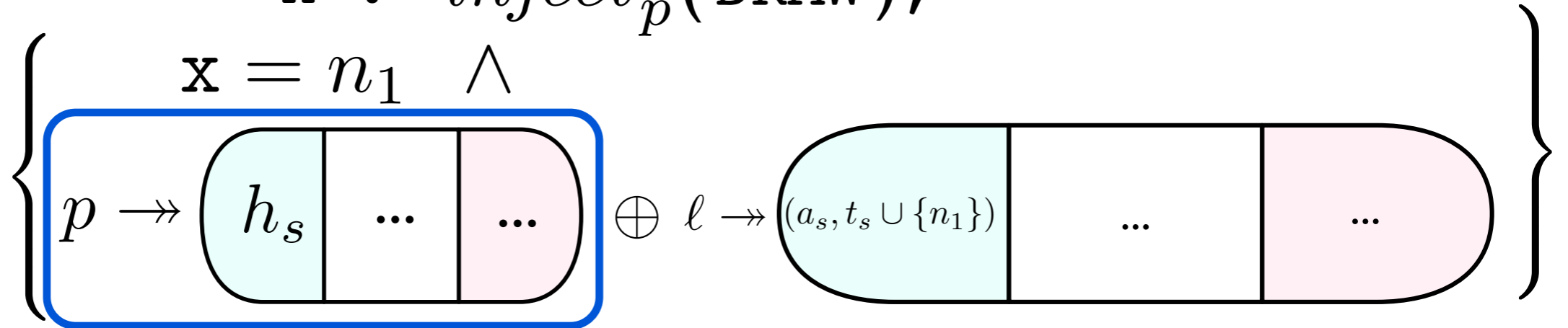
}

lock = {



$r$

$\mathbf{x} := inject_p(\text{DRAW});$



$r$

$\text{while } (!\text{TRY}(\mathbf{x})) \text{ SKIP};$

}



# Creating and disposing concurroids

# Creating and disposing resources

# CSL Resource Rule

$$\frac{\Gamma, r : I \vdash \{p\} c \{q\}}{\Gamma \vdash \{p * I\} \text{resource } r \text{ in } c \{q * I\}} \text{RESOURCECSL}$$

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$$\frac{\Gamma, r : I \vdash \{p\} c \{q\}}{\Gamma \vdash \{p * I\} \text{resource } r \text{ in } c \{q * I\}} \text{RESOURCECSL}$$

# CSL Resource Rule

$$\frac{\Gamma, r : I \vdash \{p\} c \{q\}}{\Gamma \vdash \{p * I\} \text{resource } r \text{ in } c \{q * I\}} \text{RESOURCECSL}$$

# Allocating a Ticketed Lock

```
with_tlock(owner, next, body) = {  
  owner := 0;  
  next  := 0;  
  
  hide coh(tlock ℓ(owner, next)), (aS, ∅) {  
  
    body;  
  
  }  
}
```

# Allocating a Ticketed Lock

```
with_tlock(owner, next, body) = {
```

```
  owner := 0;
```

```
  next  := 0;
```

```
  hide coh(tlock ℓ(owner, next)), (aS, ∅) {
```

```
    body;
```

```
  }
```

```
}
```

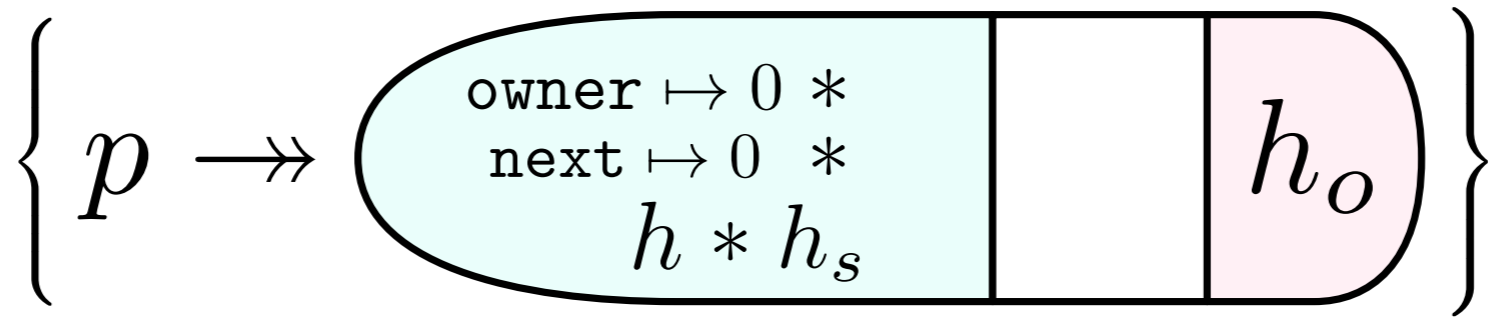
Scoped concurrent creation/disposal

*hide coh*<sub>(tlock ℓ(owner,next))</sub>, (a<sub>s</sub>, ∅) {

**body;**

}

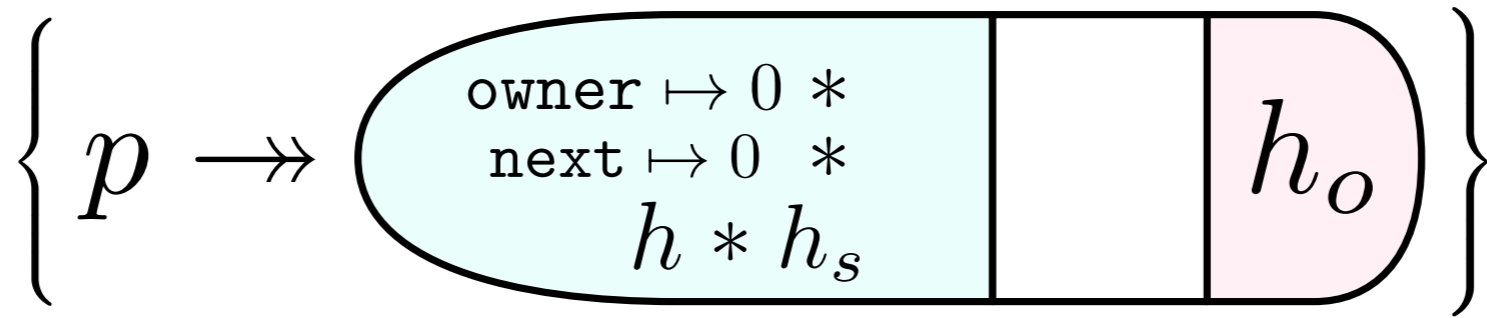




*hide coh*<sub>(tlock  $\ell(\text{owner}, \text{next})$ ), ( $a_s, \emptyset$ )</sub> {

**body;**

}

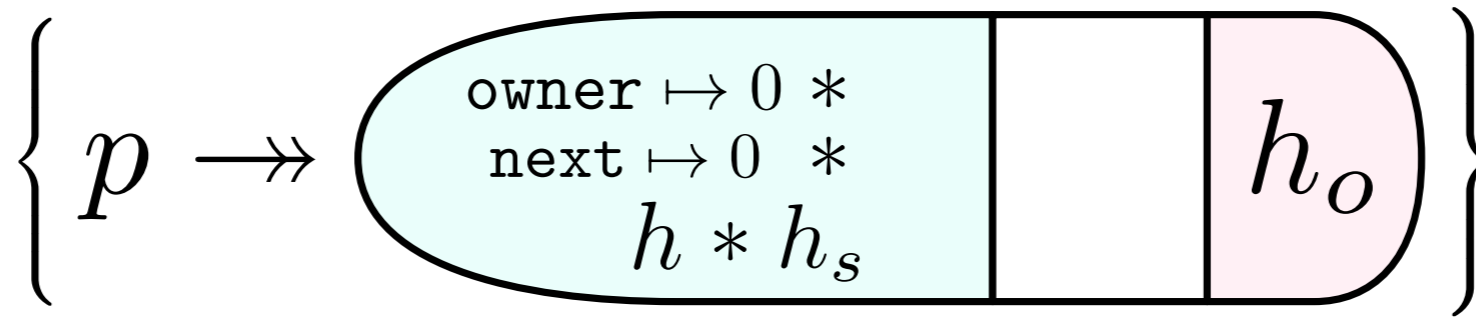


Concurroid spec

*hide*  $\text{coh}(\text{tlock } \ell(\text{owner}, \text{next}))$ ,  $(a_s, \emptyset)$  {

**body;**

}

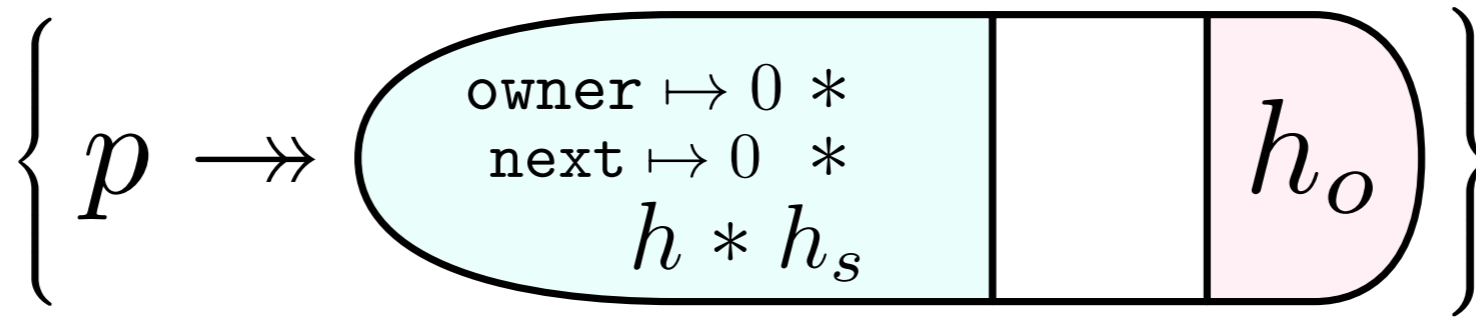


*hide* Concurroid spec Initial "self" auxiliaries

$\text{coh}(\text{tlock } \ell(\text{owner}, \text{next})), (a_s, \emptyset)$

body;

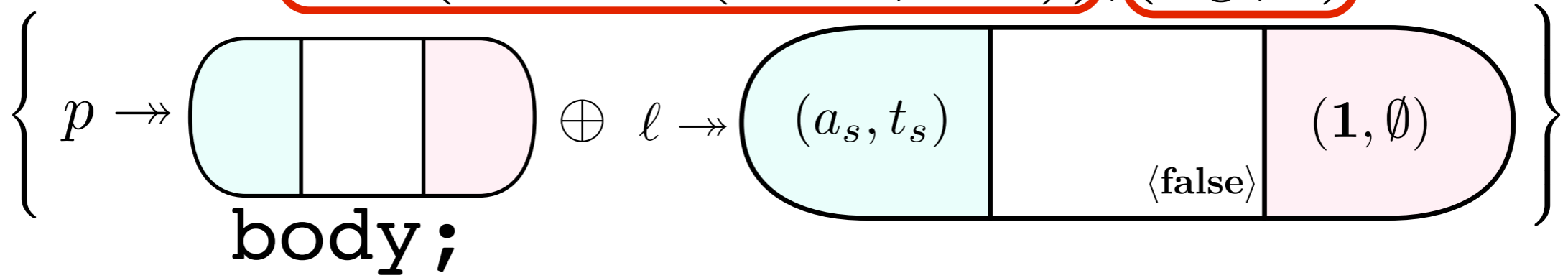
}



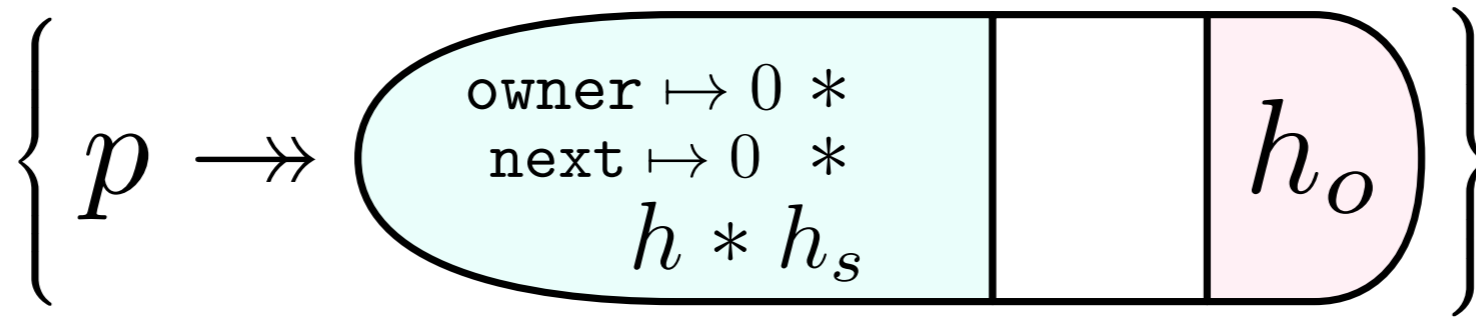
Concurroid spec

Initial "self" auxiliaries

*hide*  $\text{coh}(\text{tlock } \ell(\text{owner}, \text{next})), (a_s, \emptyset)$



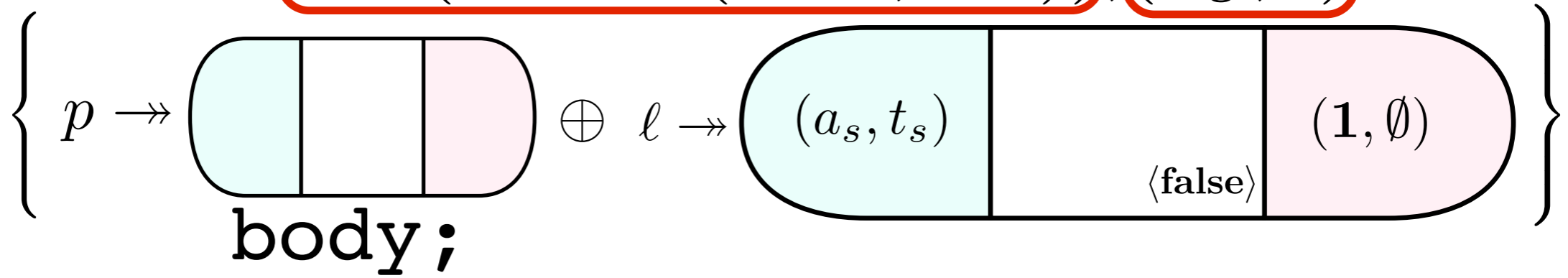
}



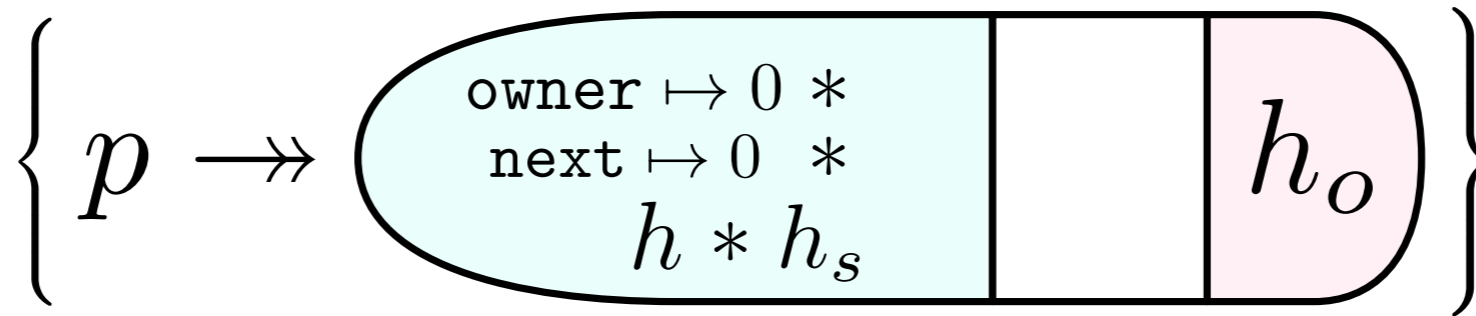
Concurroid spec

Initial "self" auxiliaries

*hide*  $\text{coh}(\text{tlock } \ell(\text{owner}, \text{next})), (a_s, \emptyset)$



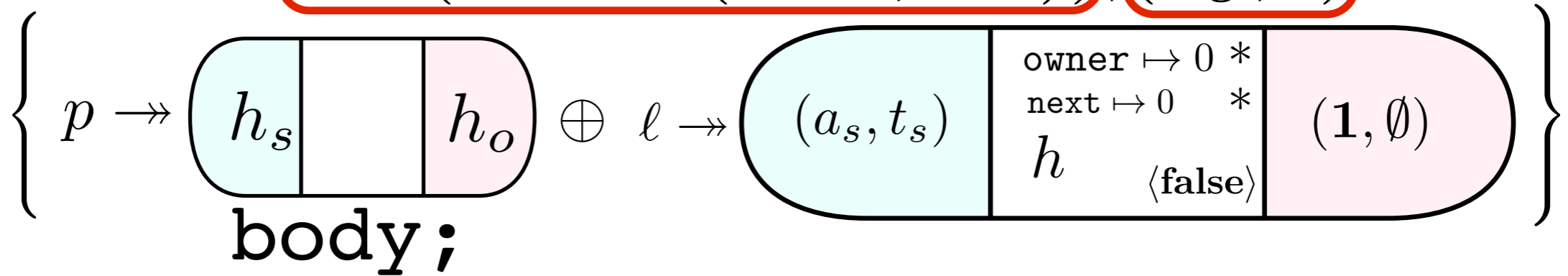
}



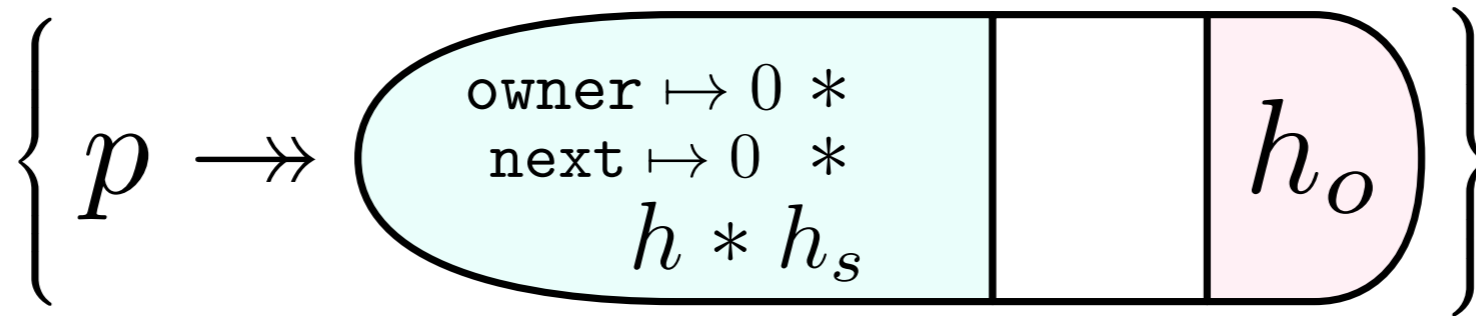
Concurroid spec

Initial "self" auxiliaries

*hide*  $\text{coh}(\text{tlock } \ell(\text{owner}, \text{next})), (a_s, \emptyset)$



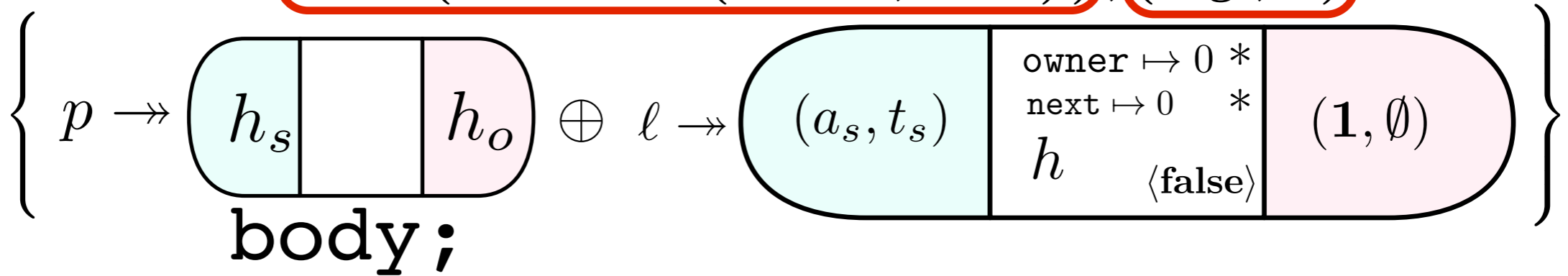
}



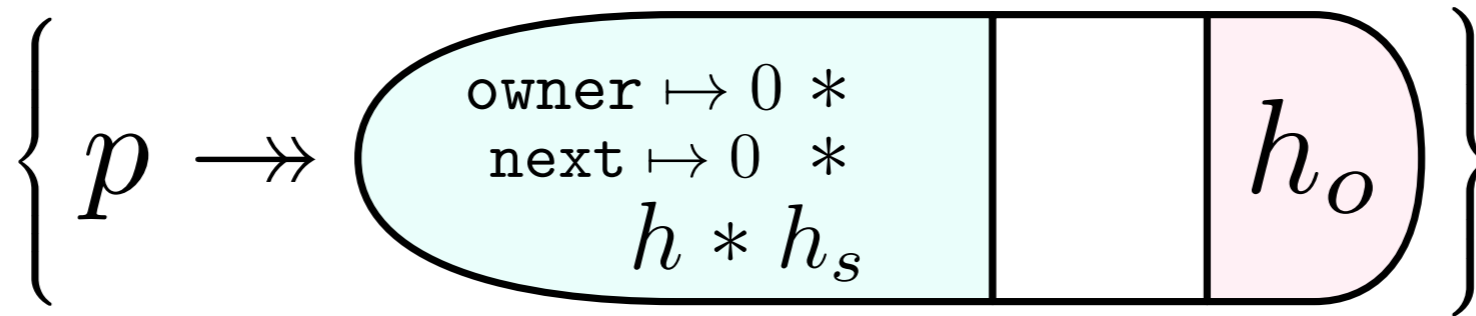
Concurroid spec

Initial "self" auxiliaries

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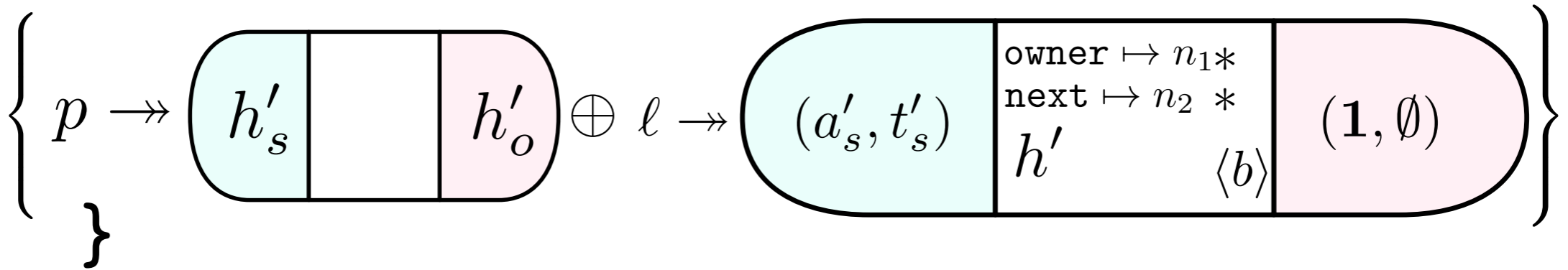
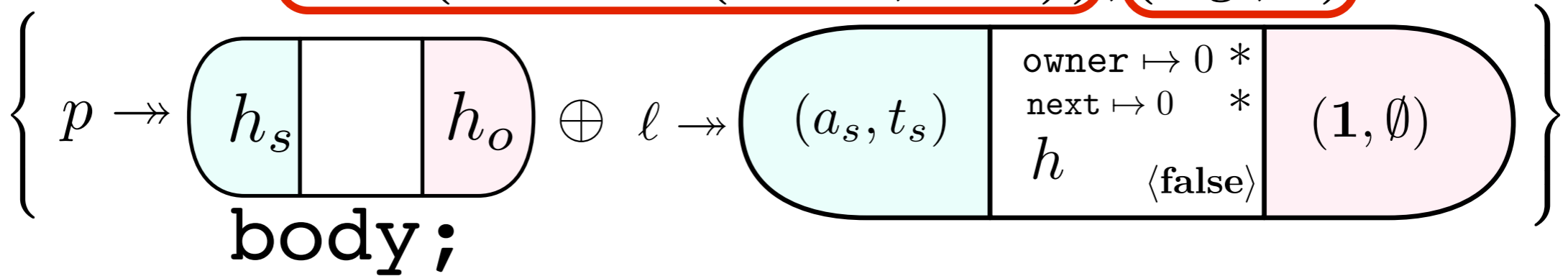
}



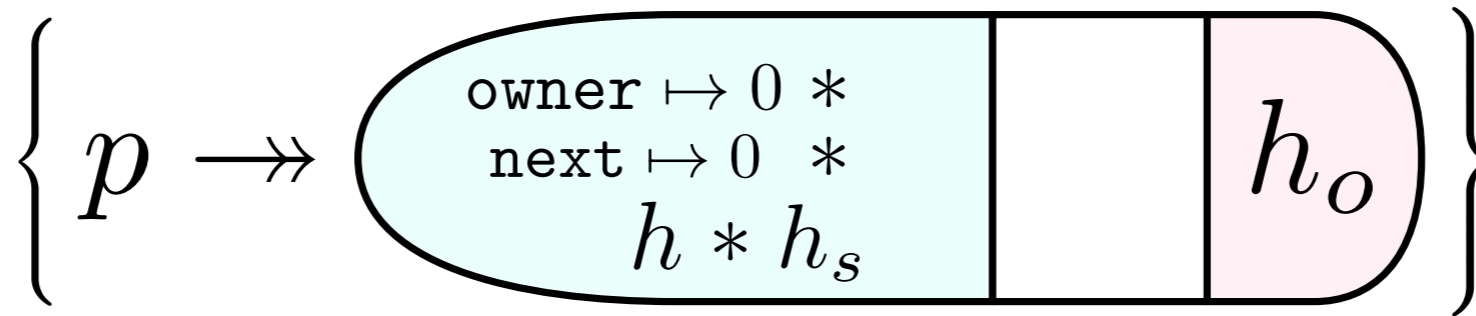
Concurroid spec

Initial "self" auxiliaries

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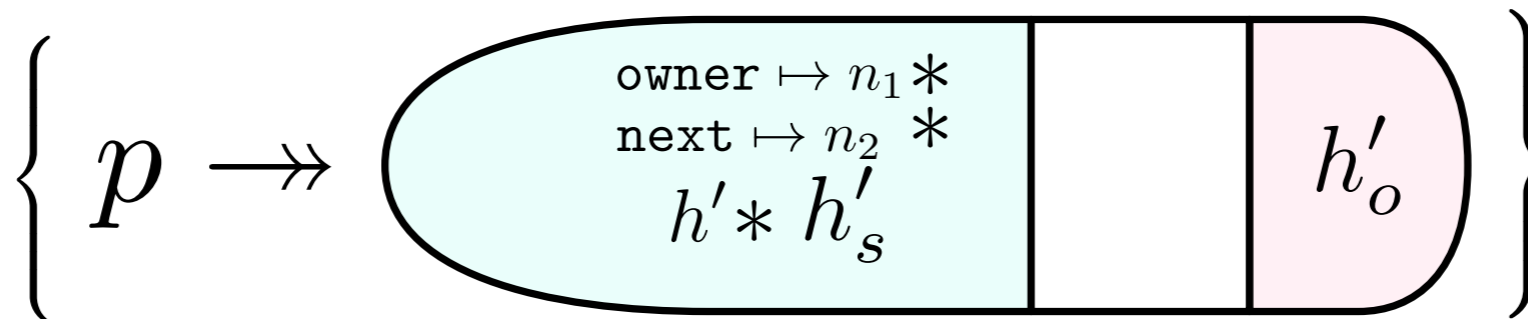
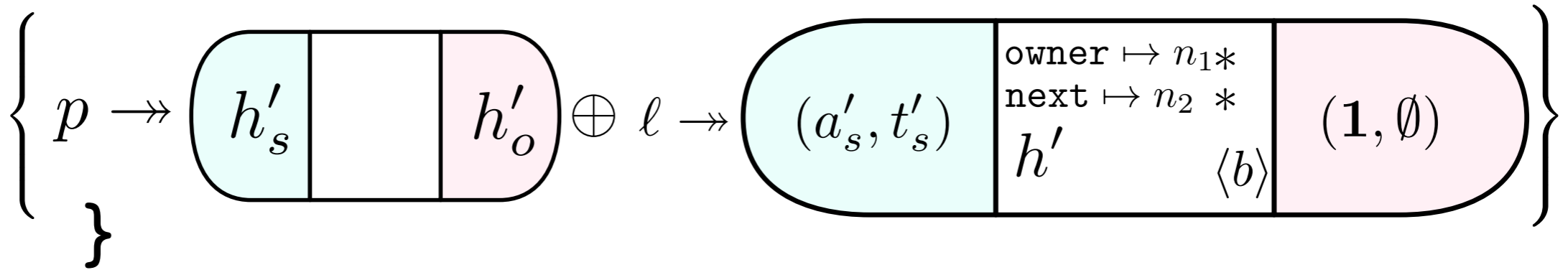
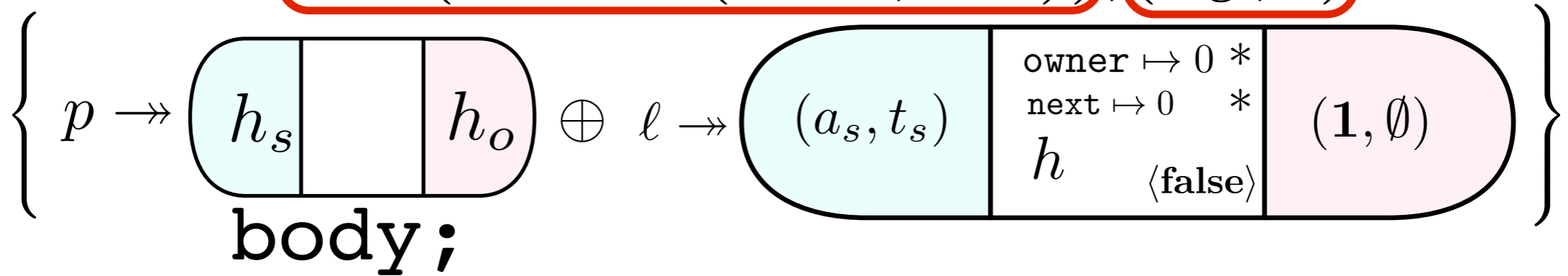




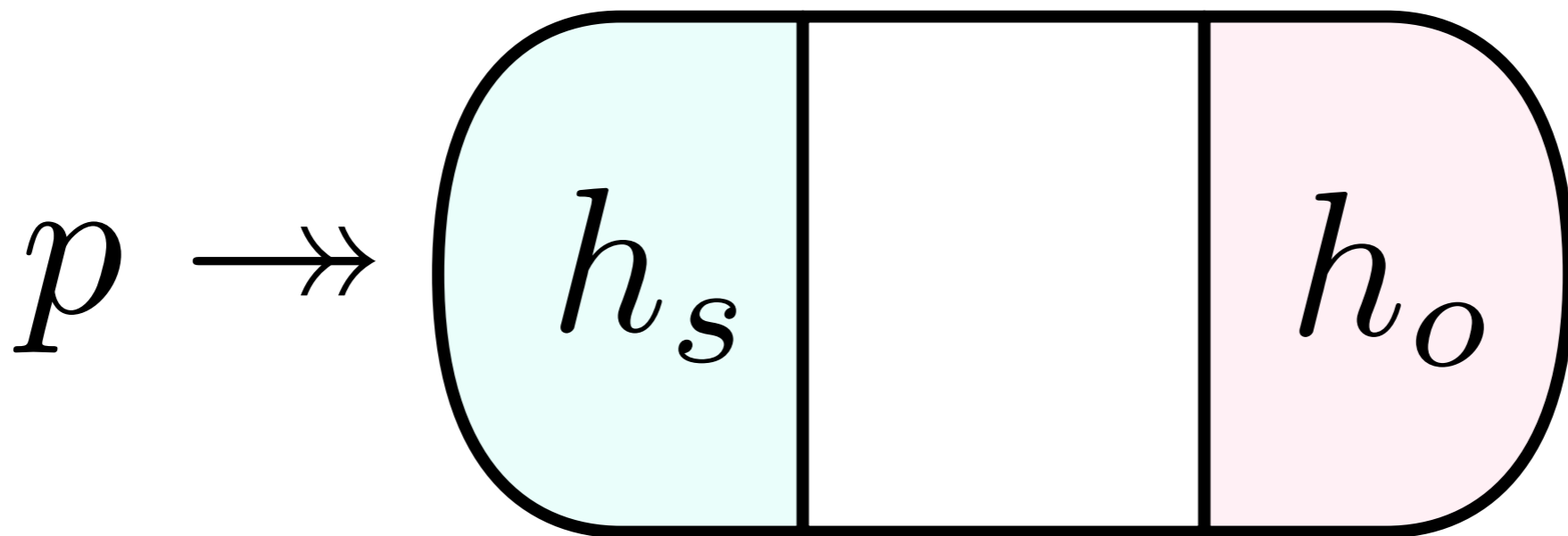
Concurroid spec

Initial "self" auxiliaries

*hide*  $\text{coh}(\text{tlock } \ell(\text{owner}, \text{next})), (a_s, \emptyset)$



# Only One Basic Concurroid



# Parallel Composition

$$\frac{\{p_1\} C_1 \{q_1\} @ U \quad \{p_2\} C_2 \{q_2\} @ U}{\{p_1 \otimes p_2\} C_1 \parallel C_2 \{q_1 \otimes q_2\} @ U} \text{PAR}$$

# Parallel Composition

$$\frac{\{p_1\} C_1 \{q_1\} @ U \quad \{p_2\} C_2 \{q_2\} @ U}{\{p_1 \circledast p_2\} C_1 \parallel C_2 \{q_1 \circledast q_2\} @ U} \text{PAR}$$

“Fork-shuffling” is handled by subjectivity: R/G are encoded by  $U$ .

# Not discussed today

- A concurroid for CAS-based lock
- A concurroid model for readers/writers
- Allocation
- Non-scoped locks
- Soundness theorem and its proof
- Abstract predicates (yes, we can do it, too)

# Implementation

- Implementation in Coq (metatheory, logic, proofs): shallow embedding into the CIC
- Higher-orderness and abstraction *for free*
- Reasoning in HTT-style: specifications are monadic types
- Some automation is done for splitting the state among concurroids
- CAS-lock and Ticketed lock are fully implemented

To take away

# To take away

**Goal I:** Model fine-grained concurrent resources with arbitrary protocols



# To take away

**Goal 1:** Model fine-grained concurrent resources with arbitrary protocols

**Goal 2:** Combine simplicity and modularity of Concurrent Separation Logic with the power of Rely-Guarantee reasoning

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**Concurroids**

“fine-grained resources”

# To take away

**Goal 1:** Model fine-grained concurrent resources with arbitrary protocols

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**Proposal:**

**Concurroids**

“fine-grained resources”

- State Transition Systems
- Communication
- Subjectivity

# To take away

**Goal 1:** Model fine-grained concurrent resources with arbitrary protocols

**Goal 2:** Combine simplicity and modularity of Concurrent Separation Logic with the power of Rely-Guarantee reasoning

**Proposal:**

**Concurroids**

“fine-grained resources”

- State Transition Systems
- Communication
- Subjectivity

Thanks!

# Backup Slides

# Taking the best of two worlds

## CSL + FG protocols

- CSL + Permissions  
Bornat et al. [POPL'05]
- Auxiliaries for FG  
Parkinson et al. [POPL'07]
- Storable locks  
Gotsman et al. [APLAS'07]
- Concurrent Abstract Predicates (CAP)  
Dindsdale-Young et al. [ECOOP'10]
- Higher-Order CAP  
Svendsen et al. [ESOP'13]
- Impredicative CAP  
Svendsen and Birkedal [HOPE'13]
- ...

## RG + Resource composition

- Separate Assume-Guarantee (SAGL)  
Feng et al. [ESOP'07]
- RGSep  
Vafeiadis and Parkinson [CONCUR'07]
- Local RG  
Feng [POPL'09]
- Deny-Guarantee  
Dods et al. [ESOP'09]
- ...

$$RI(\text{lock}) \stackrel{\text{def}}{=} \mathbf{x} \mapsto (\mathbf{a}_s \oplus \mathbf{a}_o)$$

lock;

$\mathbf{x} := \mathbf{x} + 1;$

$\mathbf{a}_s := \mathbf{a}_s + 1;$

unlock;

lock;

$\mathbf{x} := \mathbf{x} + 1;$

$\mathbf{a}_s := \mathbf{a}_s + 1;$

unlock;

$RI(\text{lock}) \stackrel{\text{def}}{=} \mathbf{x} \mapsto (\mathbf{a}_s \oplus \mathbf{a}_o)$

$\{ \mathbf{a}_s \mapsto \mathbf{0} , \mathbf{a}_o \mapsto \mathbf{n} \}$

lock;

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lock;

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$RI(\text{lock}) \stackrel{\text{def}}{=} \mathbf{x} \mapsto (\mathbf{a}_s \oplus \mathbf{a}_o)$

$\{ \mathbf{a}_s \mapsto \mathbf{0} + \mathbf{0}, \mathbf{a}_o \mapsto \mathbf{n} \}$

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$$\{ \mathbf{a}_s \mapsto \mathbf{0} + \mathbf{0}, \mathbf{a}_o \mapsto \mathbf{n} \}$$

$$\{ \mathbf{a}_s \mapsto \mathbf{0}, \mathbf{a}_o \mapsto \mathbf{n} + \mathbf{0} \}$$

lock;

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$$\{ \mathbf{a}_s \mapsto \mathbf{0}, \mathbf{a}_o \mapsto \mathbf{n} + \mathbf{0} \}$$

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$$\{ \mathbf{a}_s \mapsto \mathbf{0} + \mathbf{0}, \mathbf{a}_o \mapsto \mathbf{n} \}$$

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lock;

$\mathbf{x} := \mathbf{x} + 1;$

$\mathbf{a}_s := \mathbf{a}_s + 1;$

unlock;

$$\{ \mathbf{a}_s \mapsto \mathbf{1}, \mathbf{a}_o \mapsto \mathbf{n}_1 \}$$

$$\{ \mathbf{a}_s \mapsto \mathbf{0}, \mathbf{a}_o \mapsto \mathbf{n} + \mathbf{0} \}$$

lock;

$\mathbf{x} := \mathbf{x} + 1;$

$\mathbf{a}_s := \mathbf{a}_s + 1;$

unlock;

$$RI(\text{lock}) \stackrel{\text{def}}{=} \mathbf{x} \mapsto (\mathbf{a}_s \oplus \mathbf{a}_o)$$

$$\{ \mathbf{a}_s \mapsto \mathbf{0} + \mathbf{0}, \mathbf{a}_o \mapsto \mathbf{n} \}$$

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$\mathbf{a}_s := \mathbf{a}_s + 1;$

unlock;

$$\{ \mathbf{a}_s \mapsto \mathbf{1}, \mathbf{a}_o \mapsto \mathbf{n}_1 \}$$

$$\{ \mathbf{a}_s \mapsto \mathbf{0}, \mathbf{a}_o \mapsto \mathbf{n} + \mathbf{0} \}$$

lock;

$\mathbf{x} := \mathbf{x} + 1;$

$\mathbf{a}_s := \mathbf{a}_s + 1;$

unlock;

$$\{ \mathbf{a}_s \mapsto \mathbf{1}, \mathbf{a}_o \mapsto \mathbf{n}_2 \}$$

$$RI(\text{lock}) \stackrel{\text{def}}{=} \mathbf{x} \mapsto (\mathbf{a}_s \oplus \mathbf{a}_o)$$

$$\{ \mathbf{a}_s \mapsto \mathbf{0} + \mathbf{0}, \mathbf{a}_o \mapsto \mathbf{n} \}$$

$$\{ \mathbf{a}_s \mapsto \mathbf{0}, \mathbf{a}_o \mapsto \mathbf{n} + \mathbf{0} \}$$

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unlock;

$$\{ \mathbf{a}_s \mapsto \mathbf{1}, \mathbf{a}_o \mapsto \mathbf{n}_1 \}$$

$$\{ \mathbf{a}_s \mapsto \mathbf{0}, \mathbf{a}_o \mapsto \mathbf{n} + \mathbf{0} \}$$

lock;

$\mathbf{x} := \mathbf{x} + 1;$

$\mathbf{a}_s := \mathbf{a}_s + 1;$

unlock;

$$\{ \mathbf{a}_s \mapsto \mathbf{1}, \mathbf{a}_o \mapsto \mathbf{n}_2 \}$$

$$\{ \mathbf{a}_s \mapsto \mathbf{1} + \mathbf{1}, \exists \mathbf{n}', \mathbf{a}_o \mapsto \mathbf{n}', \mathbf{n}_1 = \mathbf{n} + \mathbf{1}, \mathbf{n}_2 = \mathbf{n}' + \mathbf{1} \}$$

$$RI(\text{lock}) \stackrel{\text{def}}{=} \mathbf{x} \mapsto (\mathbf{a}_s \oplus \mathbf{a}_o)$$

$$\{ \mathbf{a}_s \mapsto \mathbf{0} + \mathbf{0}, \mathbf{a}_o \mapsto \mathbf{n} \}$$

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unlock;

$$\{ \mathbf{a}_s \mapsto \mathbf{1}, \mathbf{a}_o \mapsto \mathbf{n}_1 \}$$

$$\{ \mathbf{a}_s \mapsto \mathbf{0}, \mathbf{a}_o \mapsto \mathbf{n} + \mathbf{0} \}$$

lock;

$\mathbf{x} := \mathbf{x} + 1;$

$\mathbf{a}_s := \mathbf{a}_s + 1;$

unlock;

$$\{ \mathbf{a}_s \mapsto \mathbf{1}, \mathbf{a}_o \mapsto \mathbf{n}_2 \}$$

$$\{ \mathbf{a}_s \mapsto \mathbf{2}, \mathbf{a}_o \mapsto - \}$$

# Verifying Programs with Atomic Actions



# Taming Stability

# TRY ( n ) Action Specification

$$\text{TRY}(n_1)(s, s', \text{res}) \triangleq$$

$$s = p \rightarrow \left( \begin{array}{|c|c|c|} \hline h_s & & h_o \\ \hline \end{array} \oplus \ell \rightarrow \left( \begin{array}{|c|c|c|} \hline (a_s, t_s \cup \{n_1\}) & \begin{array}{l} \text{owner} \mapsto n'_1 * \\ \text{next} \mapsto n_2 * \\ h \\ \langle b \rangle \end{array} & (a_o, t_o) \\ \hline \end{array} \wedge \right)$$

if  $(n_1 = n'_1)$

$$\text{then} \left( s' = p \rightarrow \left( \begin{array}{|c|c|c|} \hline h_s \oplus h & & h_o \\ \hline \end{array} \oplus \ell \rightarrow \left( \begin{array}{|c|c|c|} \hline (a_s, t_s \cup \{n_1\}) & \begin{array}{l} \text{owner} \mapsto n_1 * \\ \text{next} \mapsto n_2 * \\ \text{emp} \\ \langle \text{true} \rangle \end{array} & (a_o, t_o) \\ \hline \end{array} \wedge \right)$$

$$I(a_s \oplus a_o)h \wedge \text{res} = \text{true}$$

$$\text{else } s' = s \wedge \text{res} = \text{false}$$

# TRY ( n ) Action Specification

$$\text{TRY}(n_1)(s, s', \text{res}) \triangleq$$

$$s = p \rightarrow \left( \begin{array}{|c|c|c|} \hline h_s & & h_o \\ \hline \end{array} \oplus \ell \rightarrow \left( \begin{array}{|c|c|c|} \hline (a_s, t_s \cup \{n_1\}) & \begin{array}{l} \text{owner} \mapsto n'_1 * \\ \text{next} \mapsto n_2 * \\ h \\ \langle b \rangle \end{array} & (a_o, t_o) \\ \hline \end{array} \wedge \right) \right)$$

if  $(n_1 = n'_1)$

$$\text{then} \left( s' = p \rightarrow \left( \begin{array}{|c|c|c|} \hline h_s \oplus h & & h_o \\ \hline \end{array} \oplus \ell \rightarrow \left( \begin{array}{|c|c|c|} \hline (a_s, t_s \cup \{n_1\}) & \begin{array}{l} \text{owner} \mapsto n_1 * \\ \text{next} \mapsto n_2 * \\ \text{emp} \\ \langle \text{true} \rangle \end{array} & (a_o, t_o) \\ \hline \end{array} \wedge \right) \right) \right)$$

$$I(a_s \oplus a_o)h \wedge \text{res} = \text{true}$$

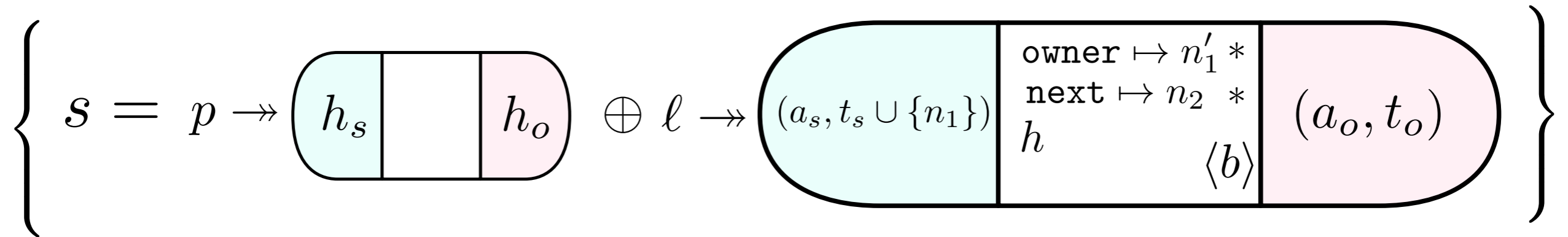
$$\text{else } s' = s \wedge \text{res} = \text{false}$$

**Not stable!**

# Stable Hoare-style Rule for TRY ( n )

$\text{TRY}(n_1)$

# Stable Hoare-style Rule for TRY (n)



TRY( $n_1$ )

# Stable Hoare-style Rule for TRY (n)

$$\left\{ s = p \rightarrow \left( \begin{array}{|c|c|c|} \hline h_s & & h_o \\ \hline \end{array} \oplus \ell \rightarrow \left( \begin{array}{|c|c|c|} \hline (a_s, t_s \cup \{n_1\}) & \begin{array}{l} \text{owner} \mapsto n'_1 * \\ \text{next} \mapsto n_2 * \\ h \end{array} & (a_o, t_o) \\ \hline \end{array} \right) \right\}$$

TRY( $n_1$ )

$$\left\{ \begin{array}{l} \text{if } (n_1 = n'_1) \\ \text{then } \left( \begin{array}{l} \exists h'_o, n'_2, t'_o, \\ s' = p \rightarrow \left( \begin{array}{|c|c|c|} \hline h_s \oplus h & & h'_o \\ \hline \end{array} \oplus \ell \rightarrow \left( \begin{array}{|c|c|c|} \hline (a_s, t_s \cup \{n_1\}) & \begin{array}{l} \text{owner} \mapsto n_1 * \\ \text{next} \mapsto n'_2 * \\ \text{emp} \end{array} & (a_o, t'_o) \\ \hline \end{array} \right) \wedge \\ I(a_s \oplus a_o)h \wedge \text{res} = \mathbf{true} \wedge \text{coh}(s') \end{array} \right) \end{array} \right\}$$

$$\left\{ \begin{array}{l} \text{else } \left( \begin{array}{l} \exists h'_o, n'_1, n'_2, t'_o, b', h', a'_o, t'_o, \\ s' = p \rightarrow \left( \begin{array}{|c|c|c|} \hline h_s & & h'_o \\ \hline \end{array} \oplus \ell \rightarrow \left( \begin{array}{|c|c|c|} \hline (a_s, t_s \cup \{n_1\}) & \begin{array}{l} \text{owner} \mapsto n'_1 * \\ \text{next} \mapsto n'_2 * \\ h' \end{array} & (a'_o, t'_o) \\ \hline \end{array} \right) \wedge \text{coh}(s') \end{array} \right) \end{array} \right\}$$

# Stable Hoare-style Rule for TRY (n)

$$\left\{ s = p \rightarrow \left( \begin{array}{|c|c|c|} \hline h_s & & h_o \\ \hline \end{array} \oplus \ell \rightarrow \left( \begin{array}{|c|c|c|} \hline (a_s, t_s \cup \{n_1\}) & \begin{array}{l} \text{owner} \mapsto n'_1 * \\ \text{next} \mapsto n_2 * \\ h \end{array} & (a_o, t_o) \\ \hline \end{array} \right) \right\}$$

TRY( $n_1$ )

if ( $n_1 = n'_1$ )

then  $\exists h'_o, n'_2, t'_o,$

$$s' = p \rightarrow \left( \begin{array}{|c|c|c|} \hline h_s \oplus h & & h'_o \\ \hline \end{array} \oplus \ell \rightarrow \left( \begin{array}{|c|c|c|} \hline (a_s, t_s \cup \{n_1\}) & \begin{array}{l} \text{owner} \mapsto n_1 * \\ \text{next} \mapsto n'_2 * \\ \text{emp} \end{array} & (a_o, t'_o) \\ \hline \end{array} \right) \wedge$$

$$I(a_s \oplus a_o)h \wedge \text{res} = \mathbf{true} \wedge \text{coh}(s')$$

else  $\exists h'_o, n'_1, n'_2, t'_o, b', h', a'_o, t'_o,$

$$s' = p \rightarrow \left( \begin{array}{|c|c|c|} \hline h_s & & h'_o \\ \hline \end{array} \oplus \ell \rightarrow \left( \begin{array}{|c|c|c|} \hline (a_s, t_s \cup \{n_1\}) & \begin{array}{l} \text{owner} \mapsto n'_1 * \\ \text{next} \mapsto n'_2 * \\ h' \end{array} & (a'_o, t'_o) \\ \hline \end{array} \right) \wedge \text{coh}(s')$$

# On the role of hiding

- *Subjective state* allows one to give a lower bound to the joint contribution:

*“I know what is my contribution.”*

- *Hiding (or scoping)* allows one to provide an upper bound for the contribution:

*“When everyone is done, we can the auxiliaries are summed up.”*



# Some General Coherence Properties

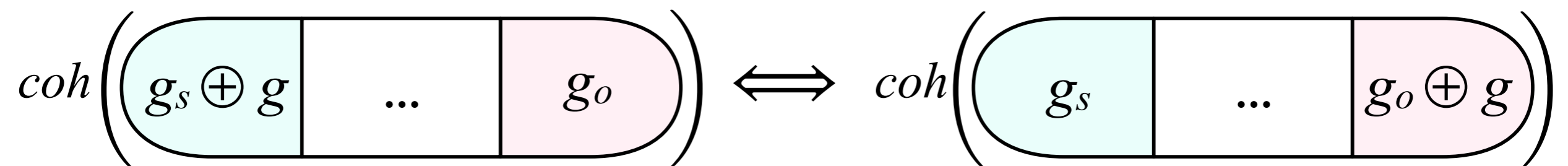
- Heap-consistency:

$h_{self} \oplus h_{other} \oplus h_{shared}$  is defined

- Self-other consistency:

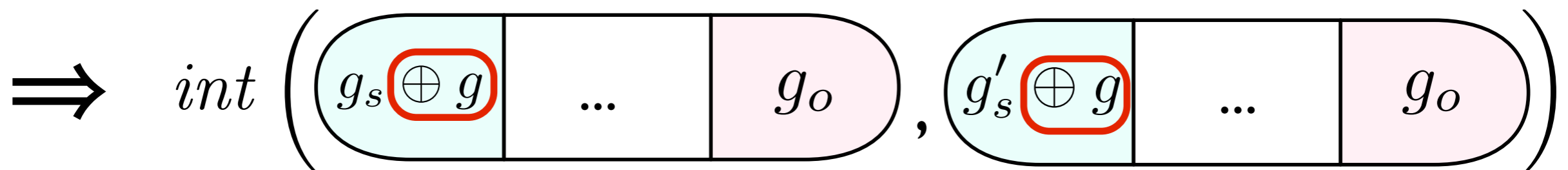
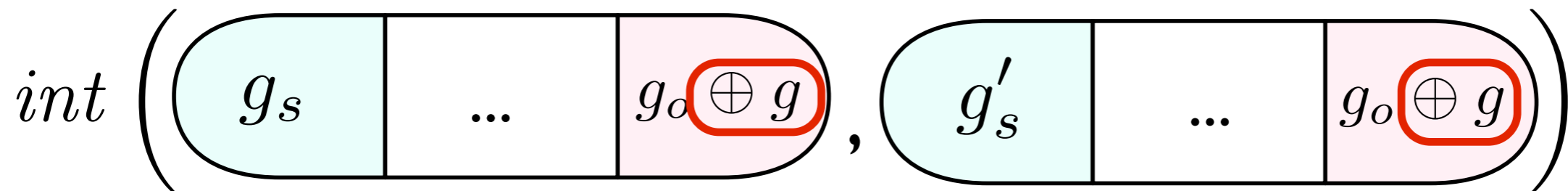
$(a_s, t_s) \oplus (a_o, t_o)$  is defined

- Fork-join consistency:



# Internal Transition Properties

- Coherence-consistency:  $int(s_1, s_2) \Rightarrow coh(s_1) \wedge coh(s_2)$
- Reflexivity
- Heap footprint preservation:  
 $int(s_1, s_2) \Rightarrow dom(heap(s_1)) = dom(heap(s_2))$
- Self-locality:  $int(s_1, s_2) \Rightarrow other(s_1) = other(s_2)$
- Fork-join consistency:



# Acquire/Release Properties

- Coherence-consistency:

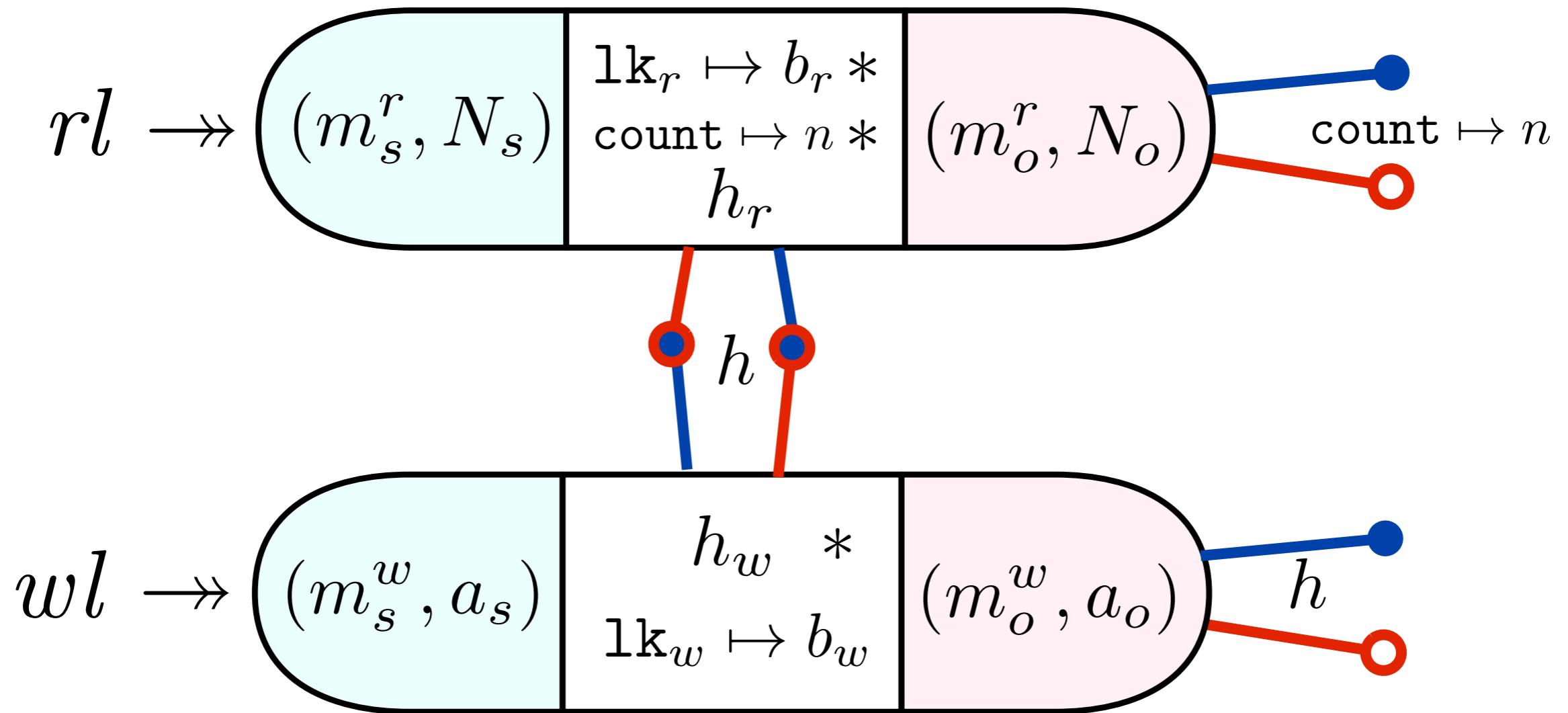
$$acq(s_1, s_2) \Rightarrow coh(s_1) \wedge coh(s_2)$$

- Self-locality:

$$acq(s_1, s_2) \Rightarrow other(s_1) = other(s_2)$$

- Fork-join consistency

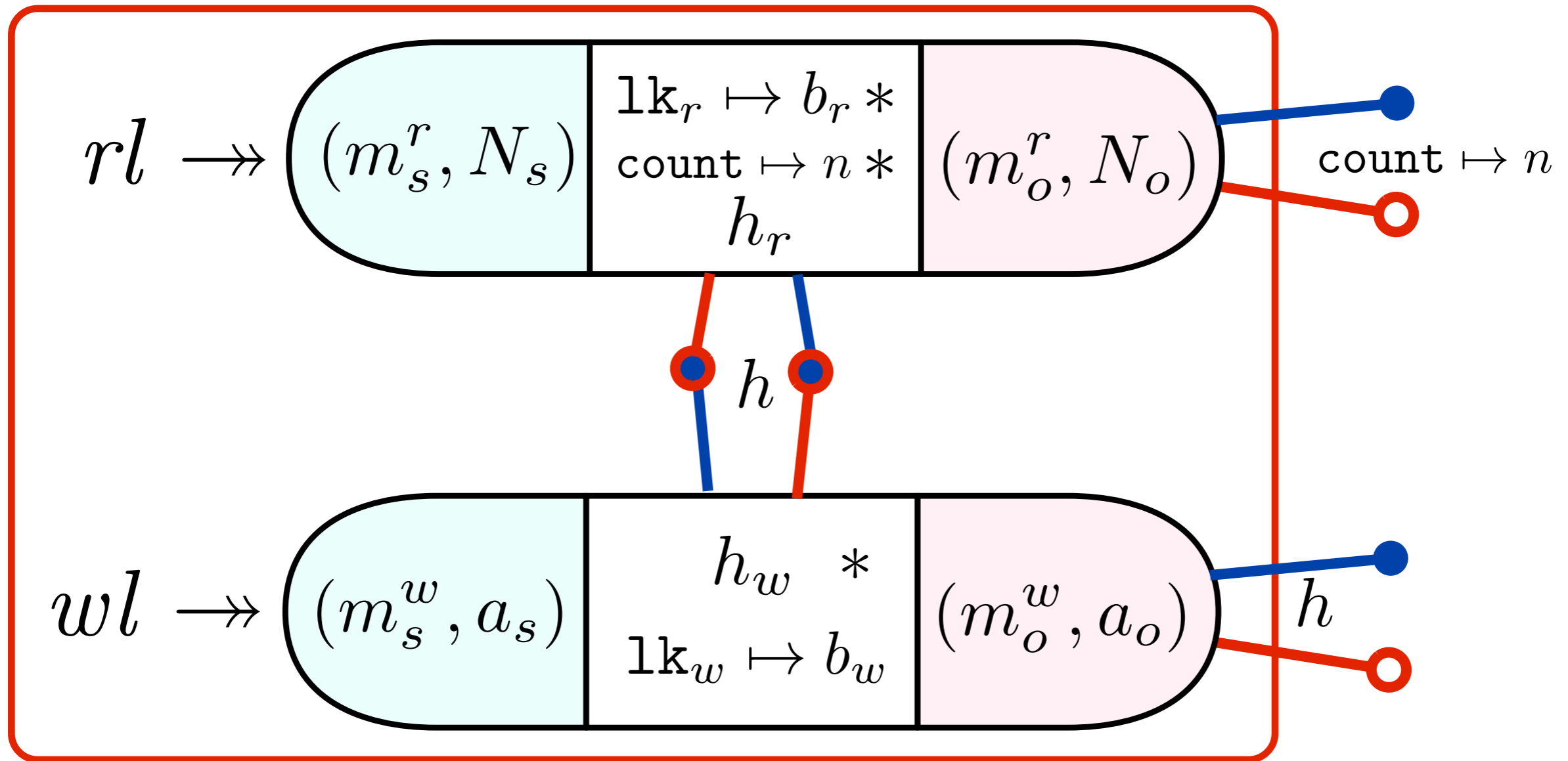
# Readers-Writers



$$I_r(N_s \oplus N_o, h_r) \triangleq (N_s \oplus N_o = n) \wedge (N_s \oplus N_o = 0 \implies h_r = \text{emp})$$

$$I_w(a_s \oplus a_o, h_w) \triangleq \dots$$

# Readers-Writers



$$I_r(N_s \oplus N_o, h_r) \triangleq (N_s \oplus N_o = n) \wedge (N_s \oplus N_o = 0 \implies h_r = \text{emp})$$

$$I_w(a_s \oplus a_o, h_w) \triangleq \dots$$